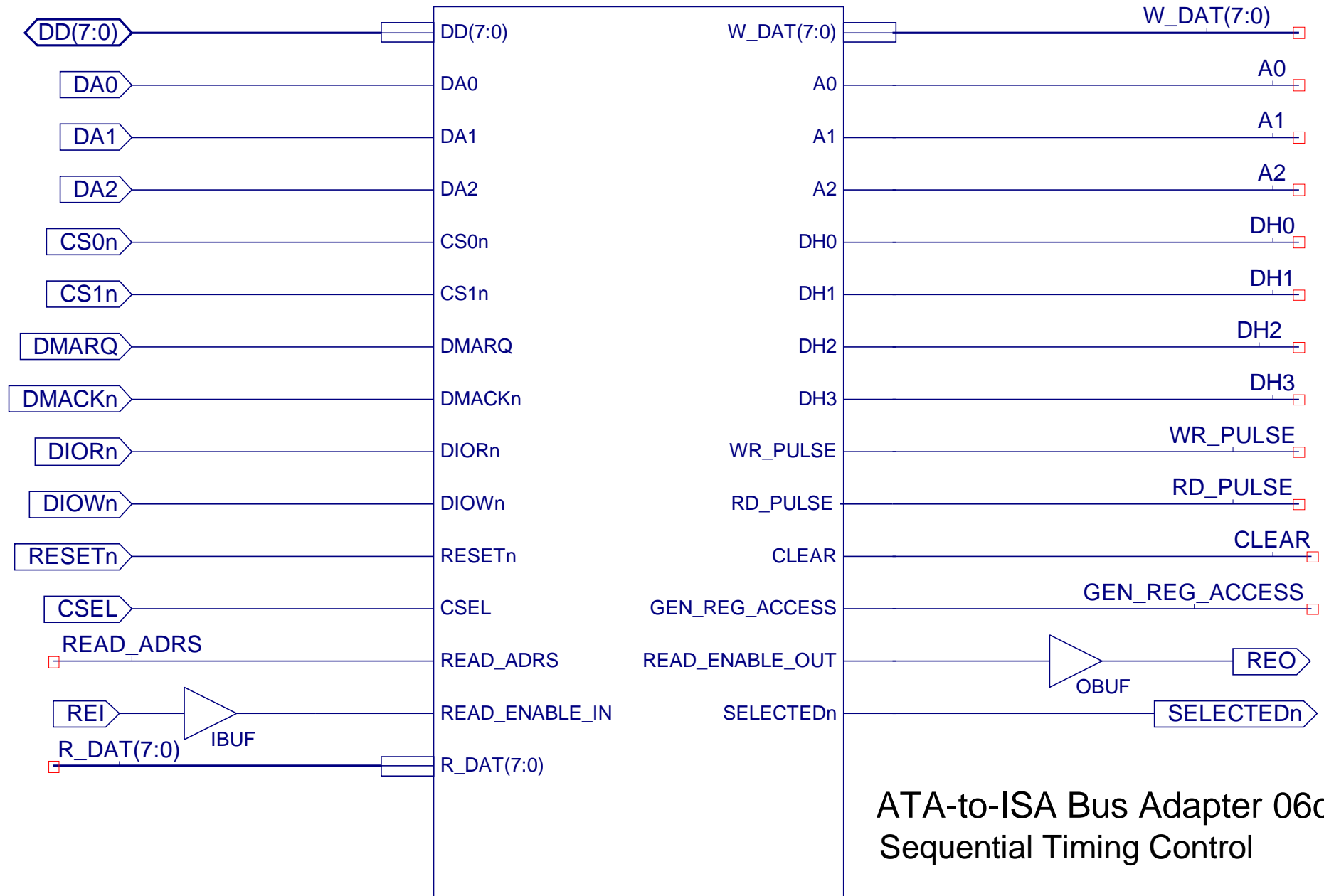
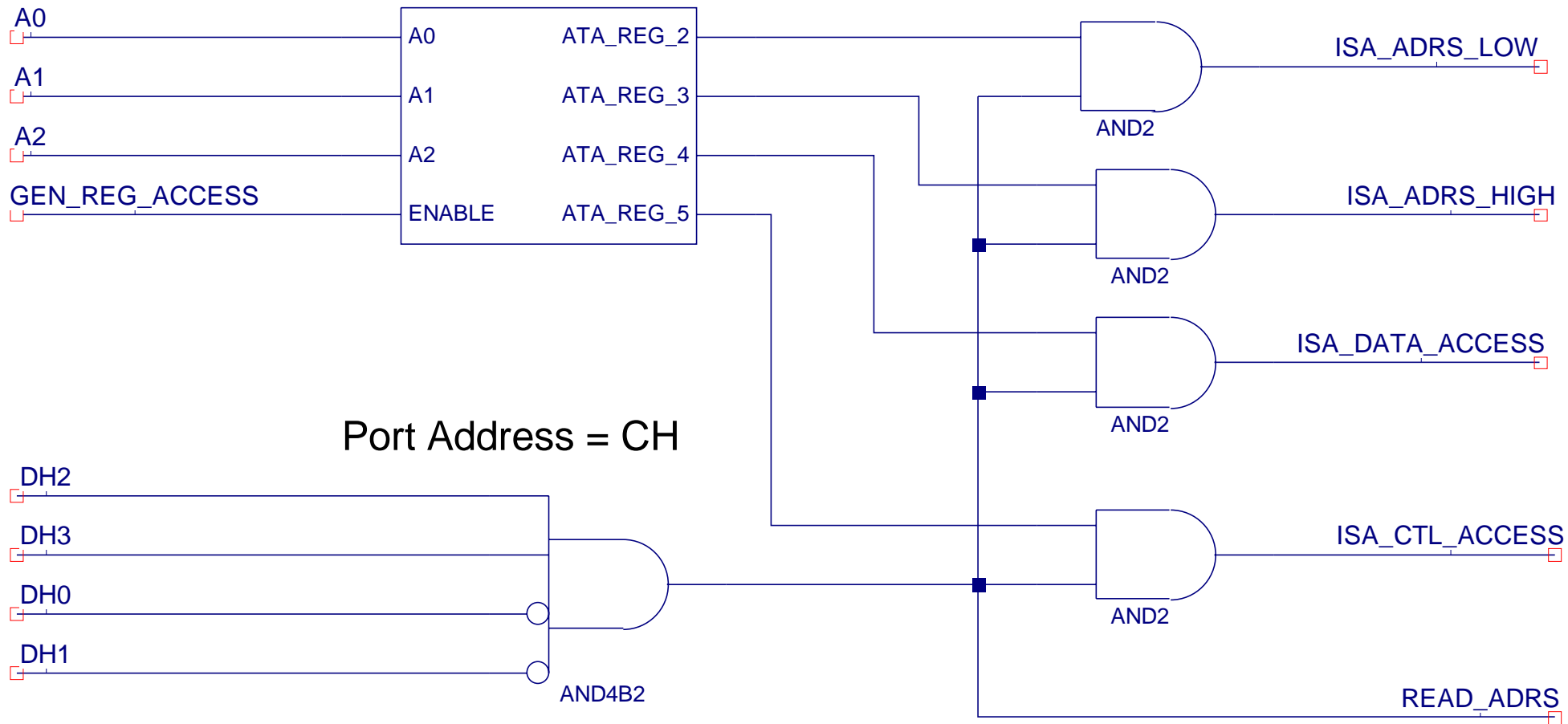


# ATA\_FRONTEND\_01d



# ATA\_REG\_DECODE



FD8CE

OBUF8

FD8CE

OBUF8

FD8CE

W\_DAT(7:0)

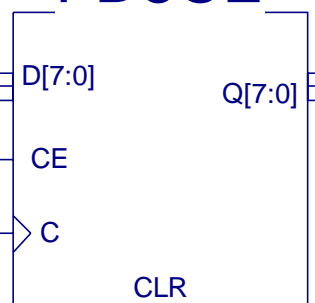
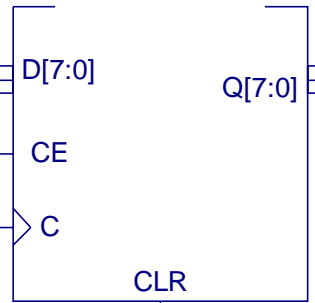
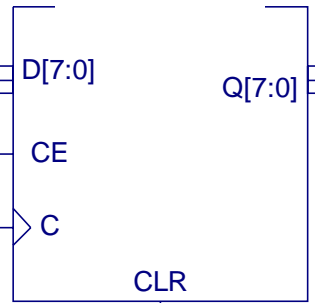
ISA\_ADRS\_LOW

WR\_PULSE

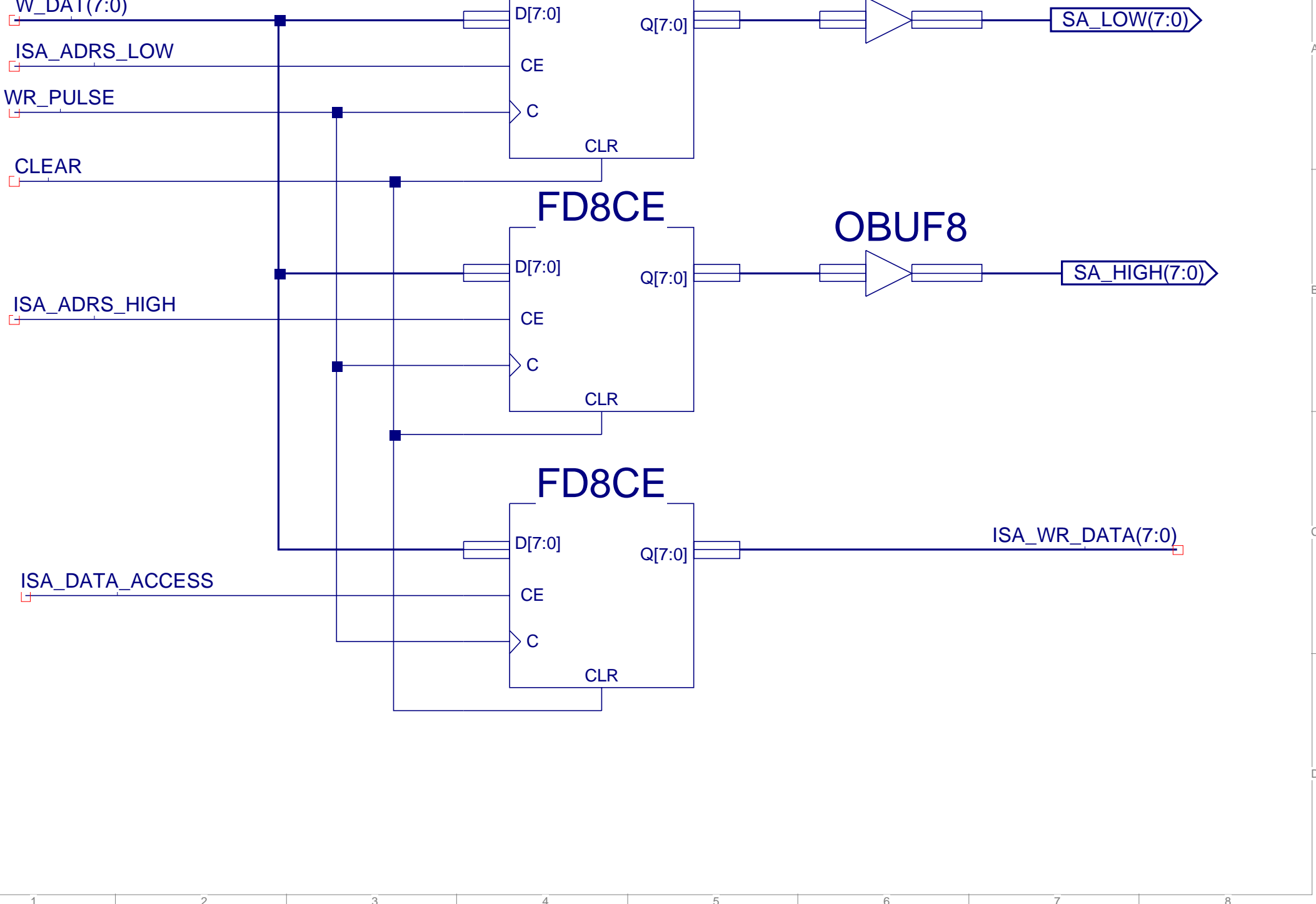
CLEAR

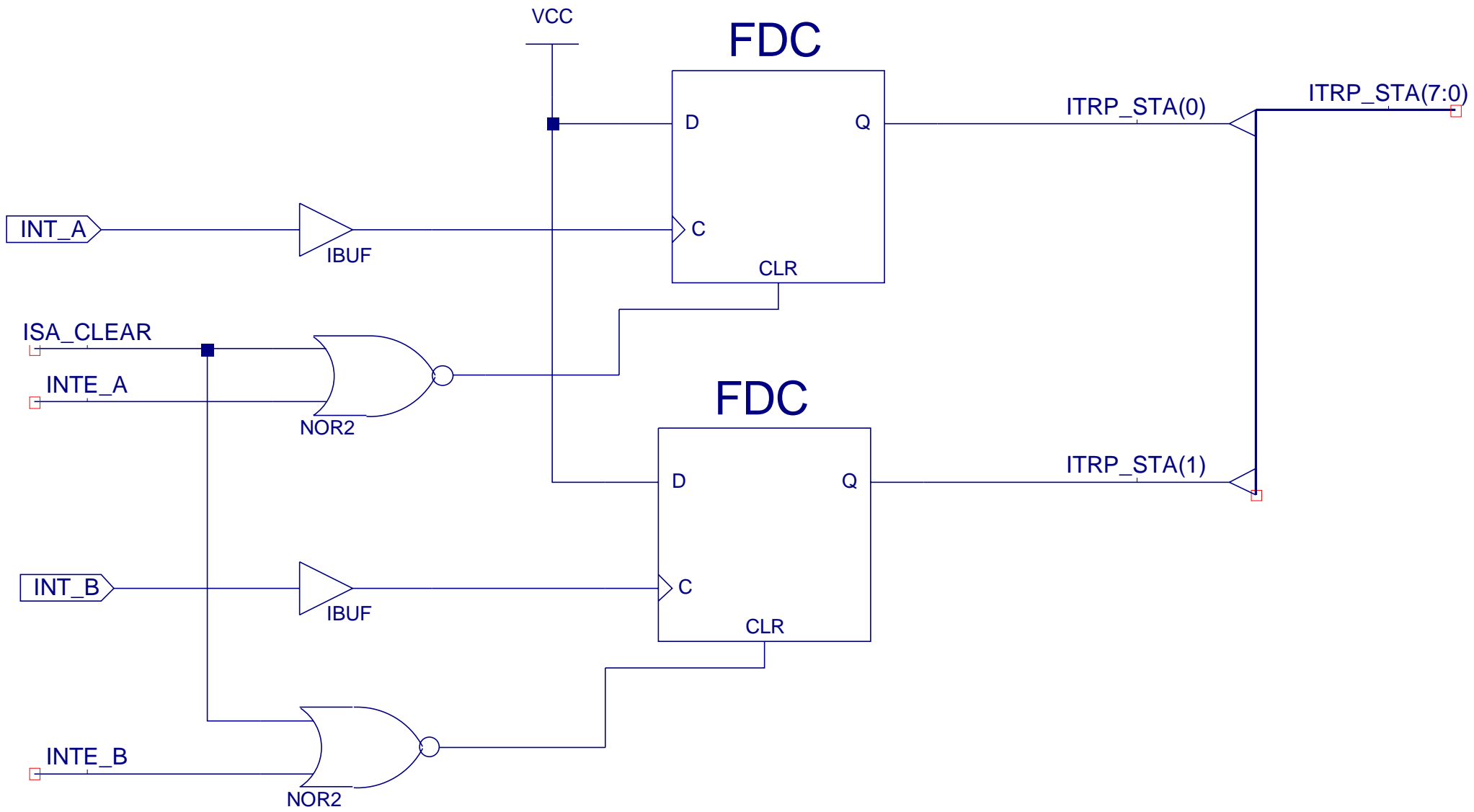
ISA\_ADRS\_HIGH

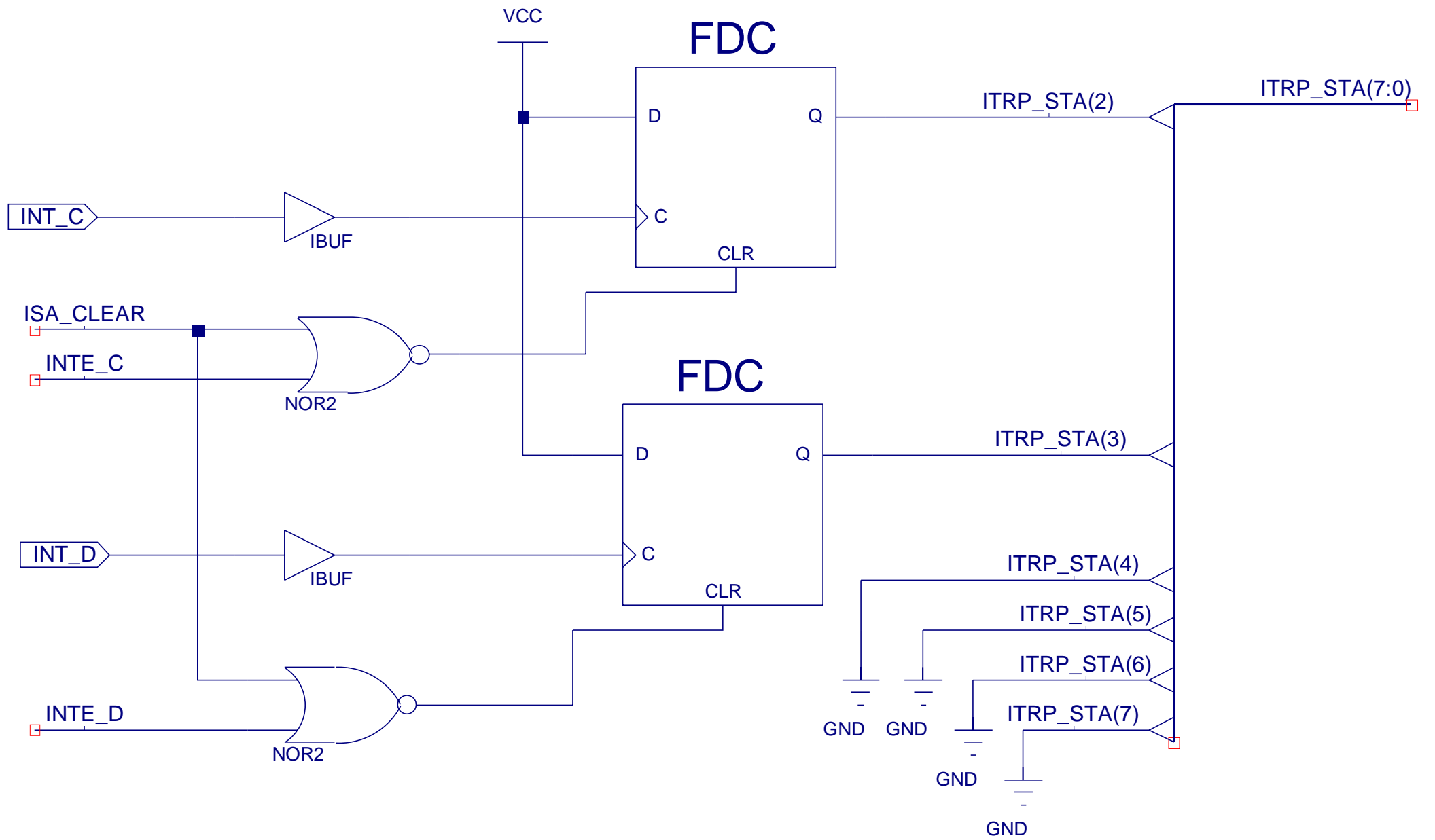
ISA\_DATA\_ACCESS



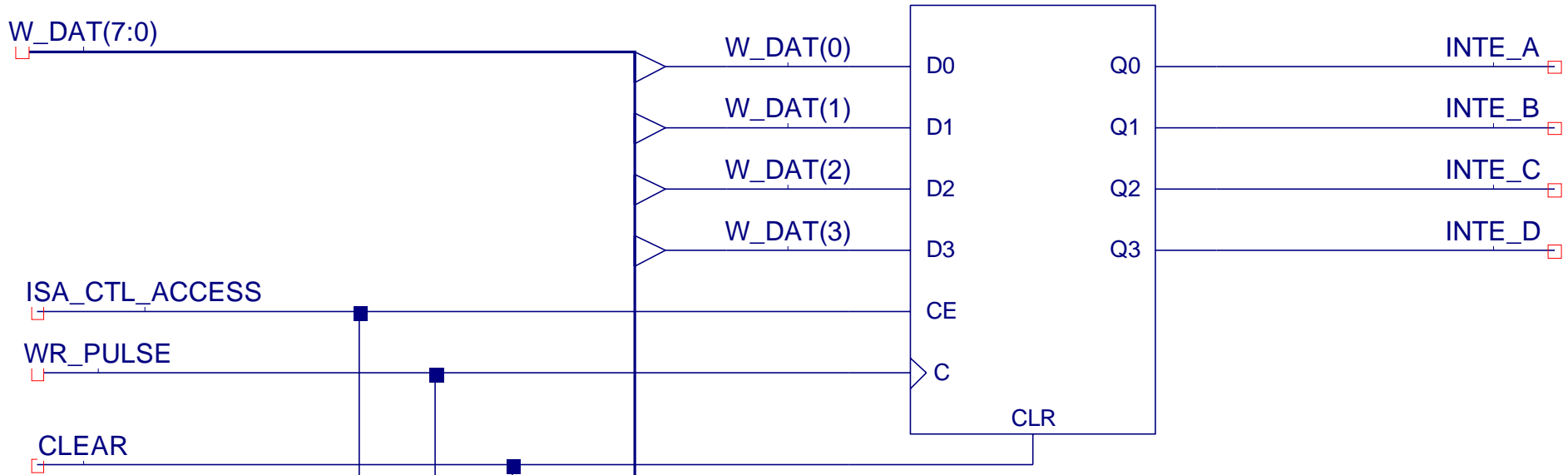
ISA\_WR\_DATA(7:0)



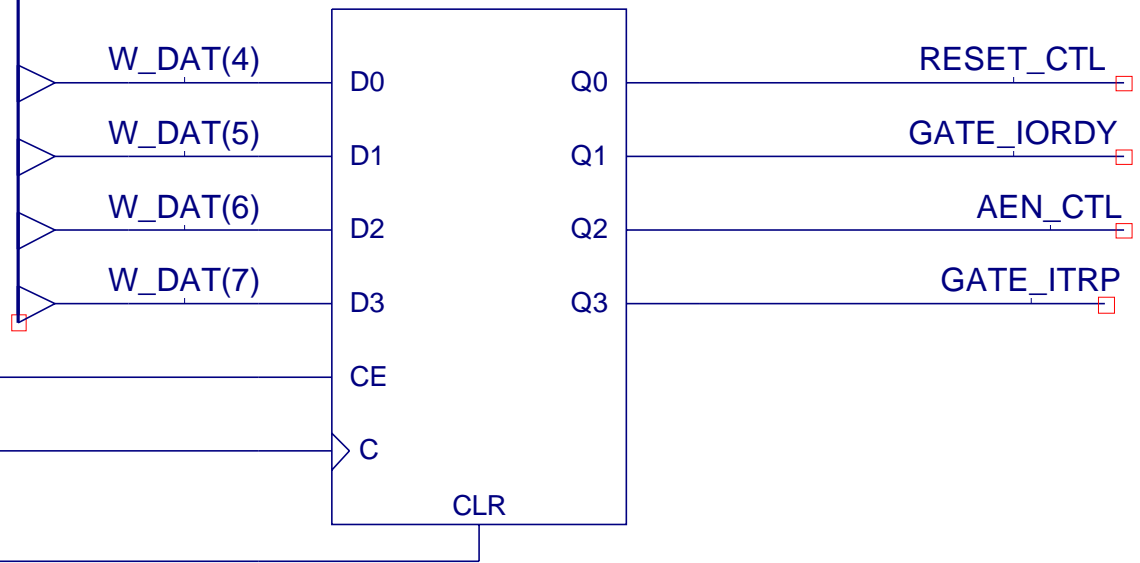


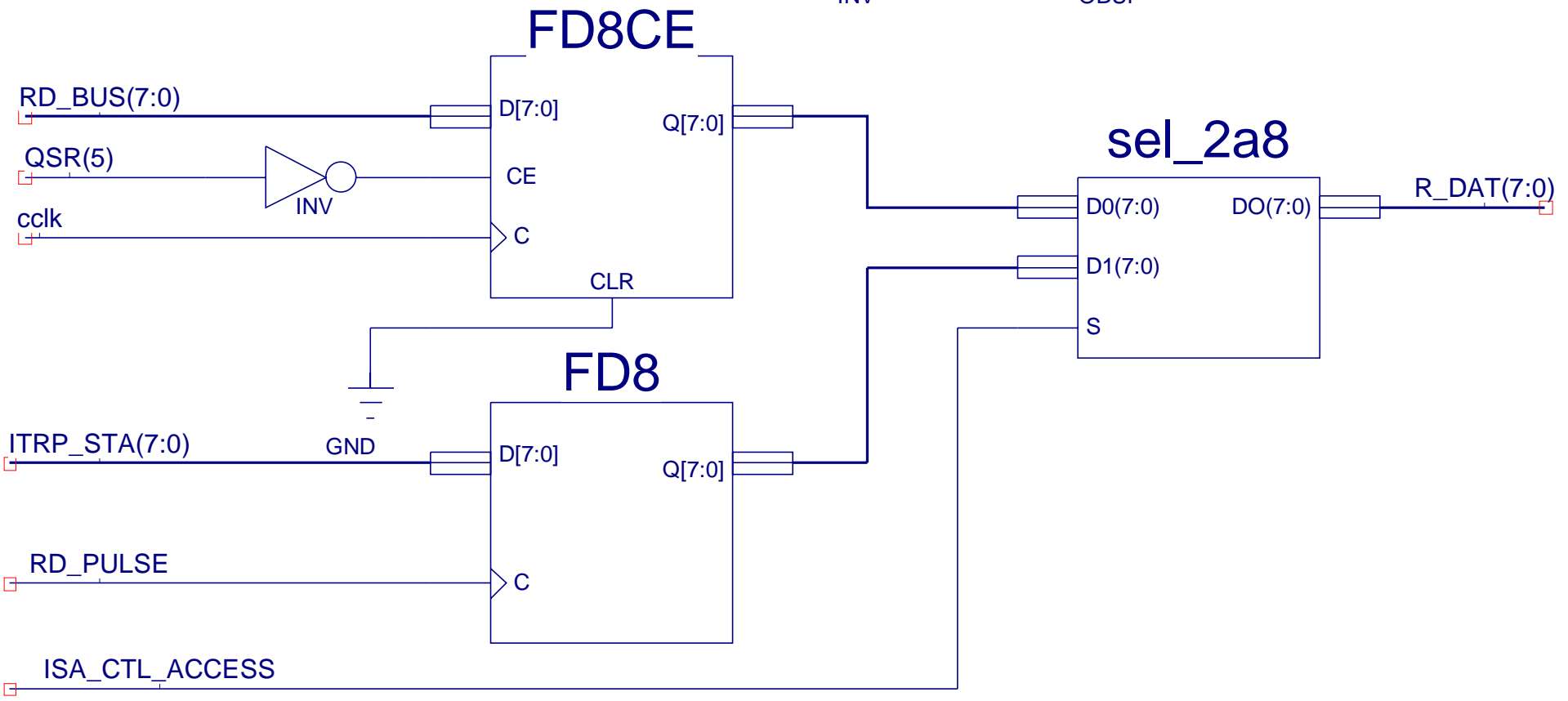
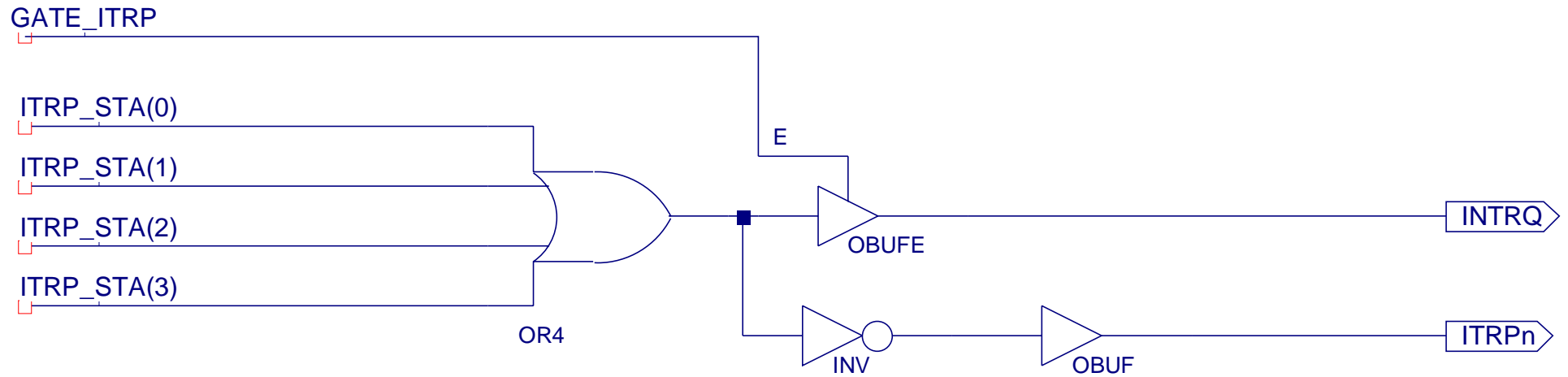


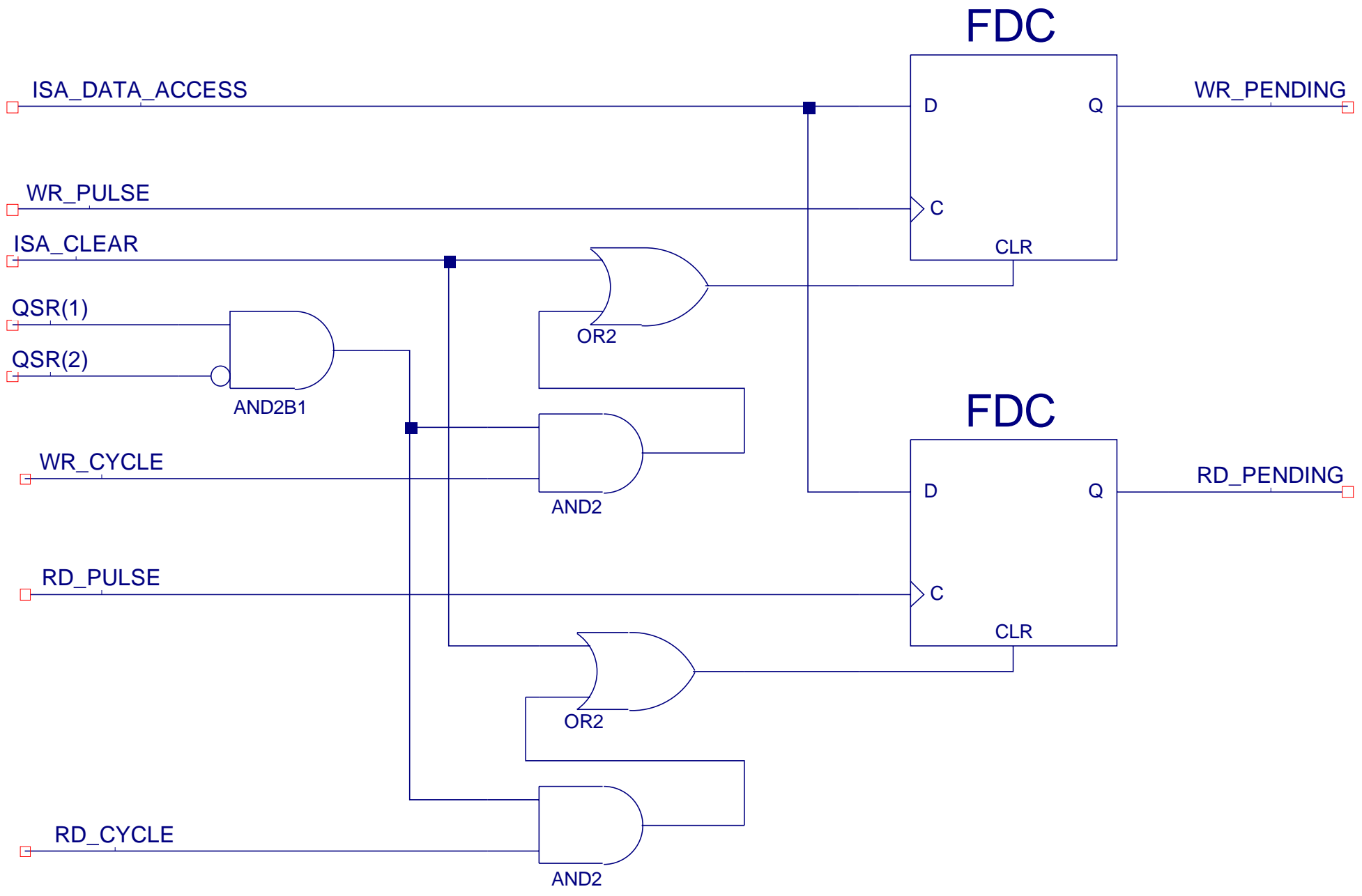
# FD4CE



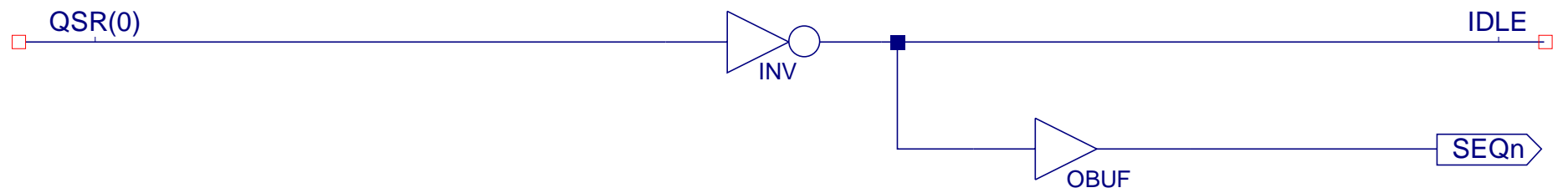
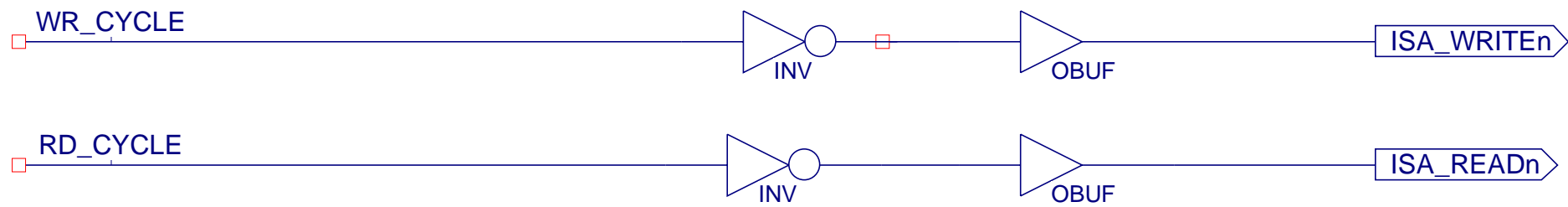
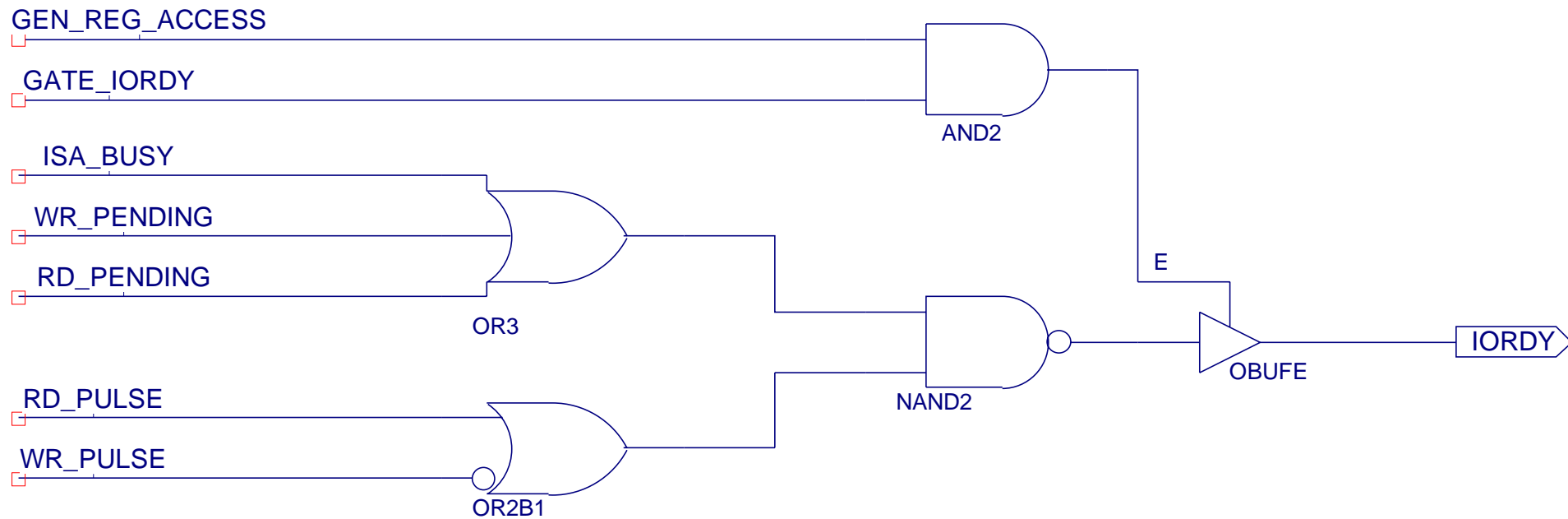
# FD4CE

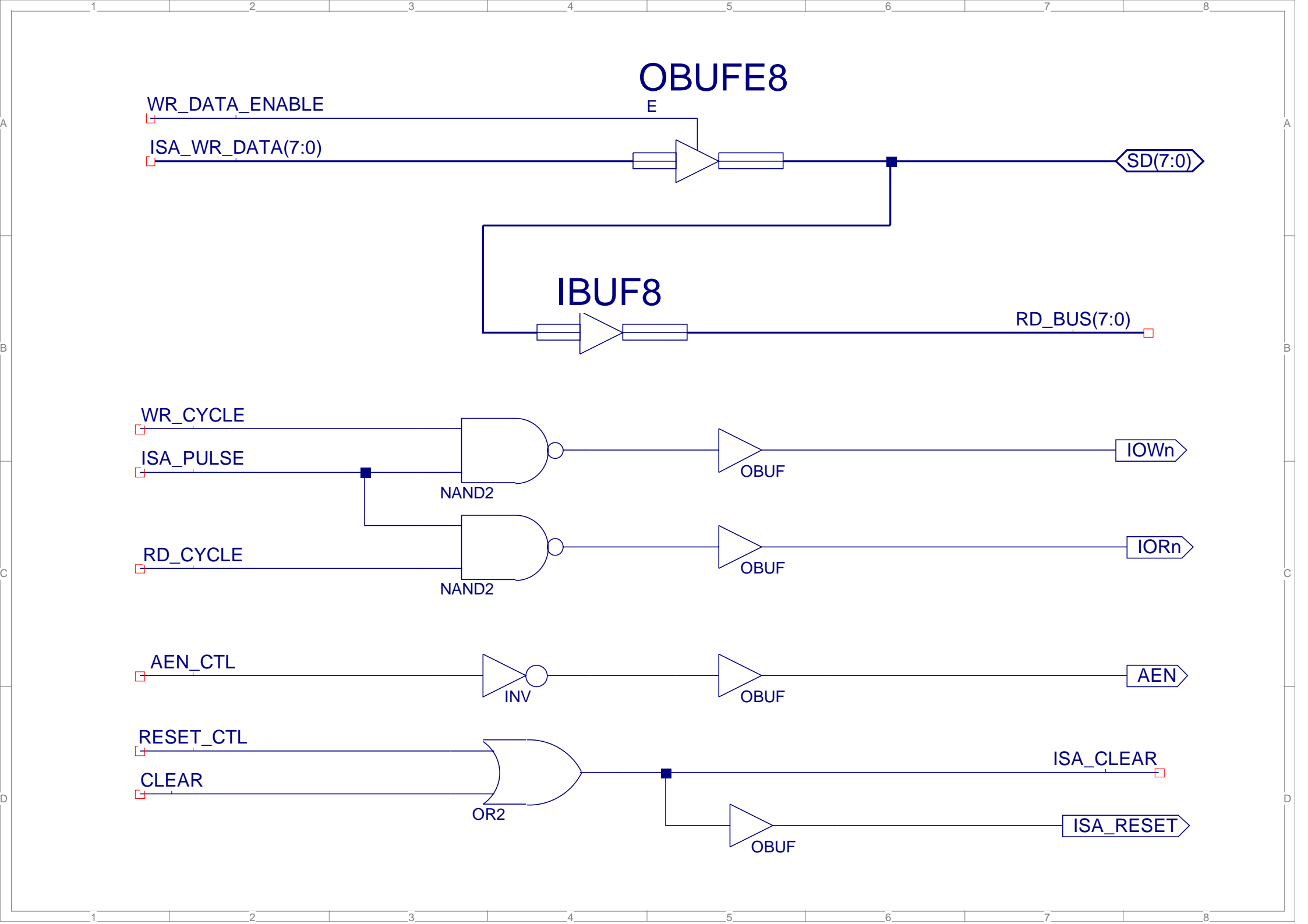












# SR8CLE

