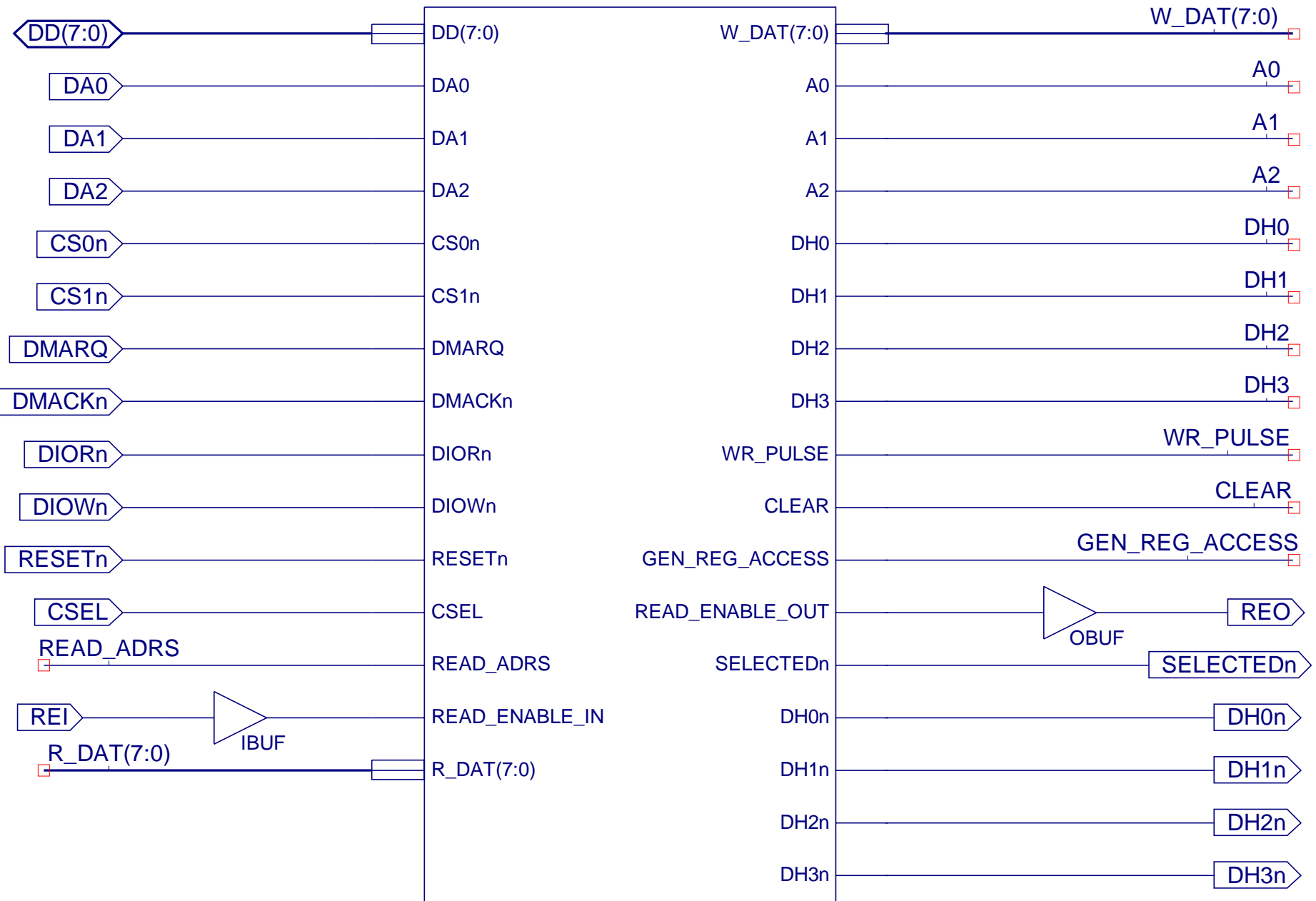


ATA_FRONTEND_01a



GEN_REG_ACCESS

A2

A1

A0

AND4B2

DAT_ACCESS

AND4B1

DIR_ACCESS

DH3

AND2B2

READ_ADRS

DH2

DH1

AND2

