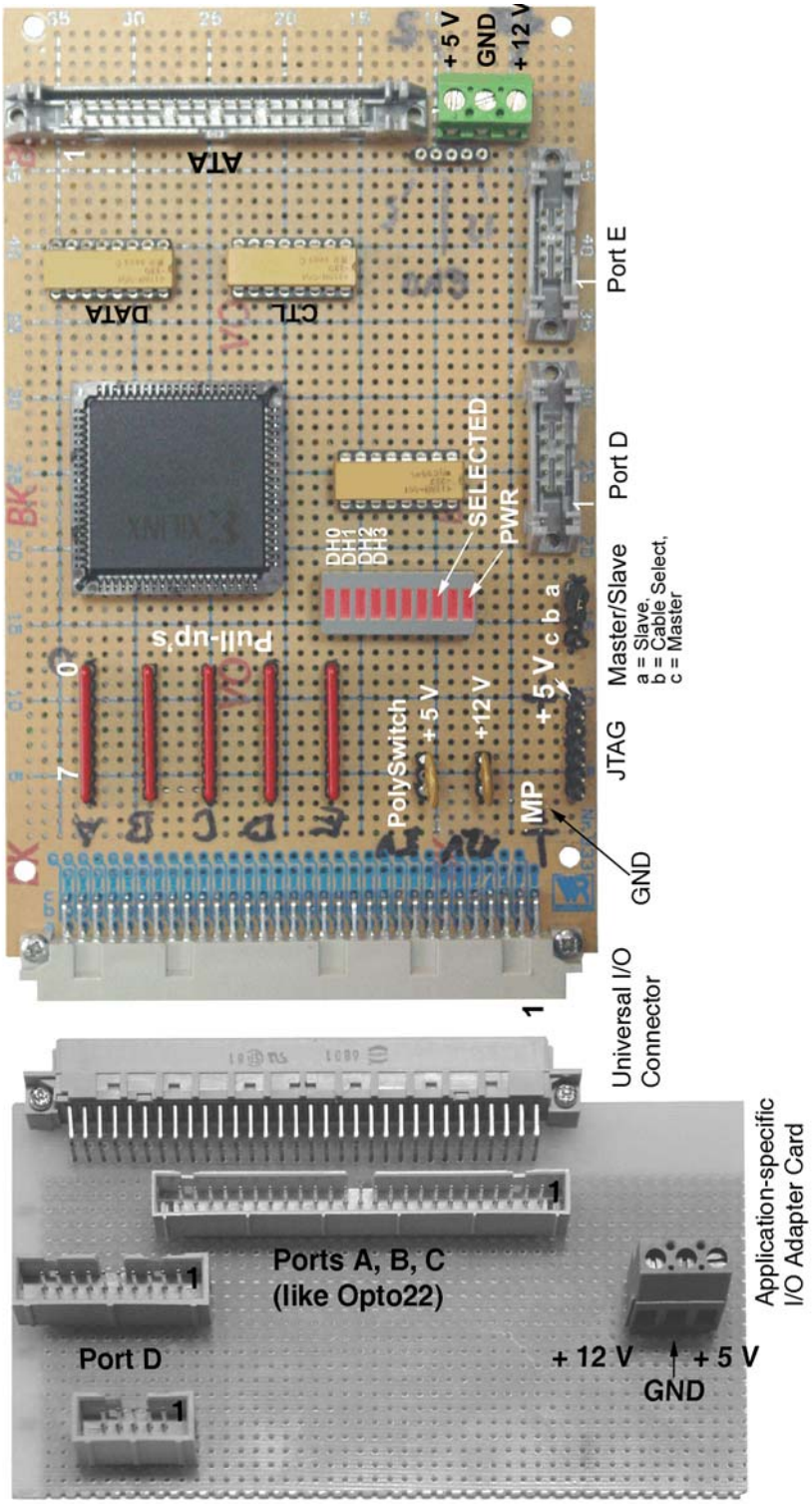
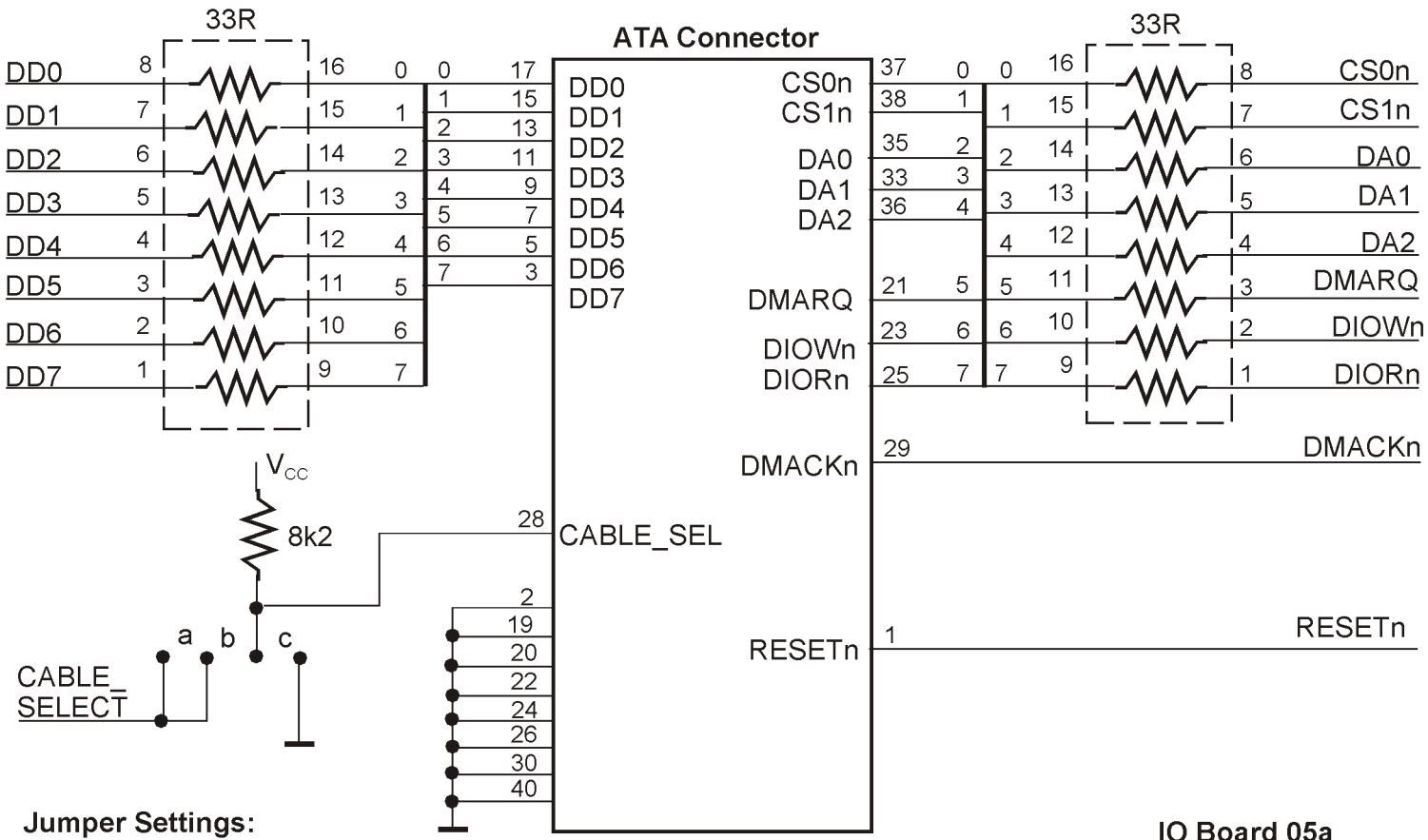
ATA-to-ISA Board 07a

– Schematics –

This experimental board contains an ATA IO port adapter 05a in a Xilinx 95108 CPLD.

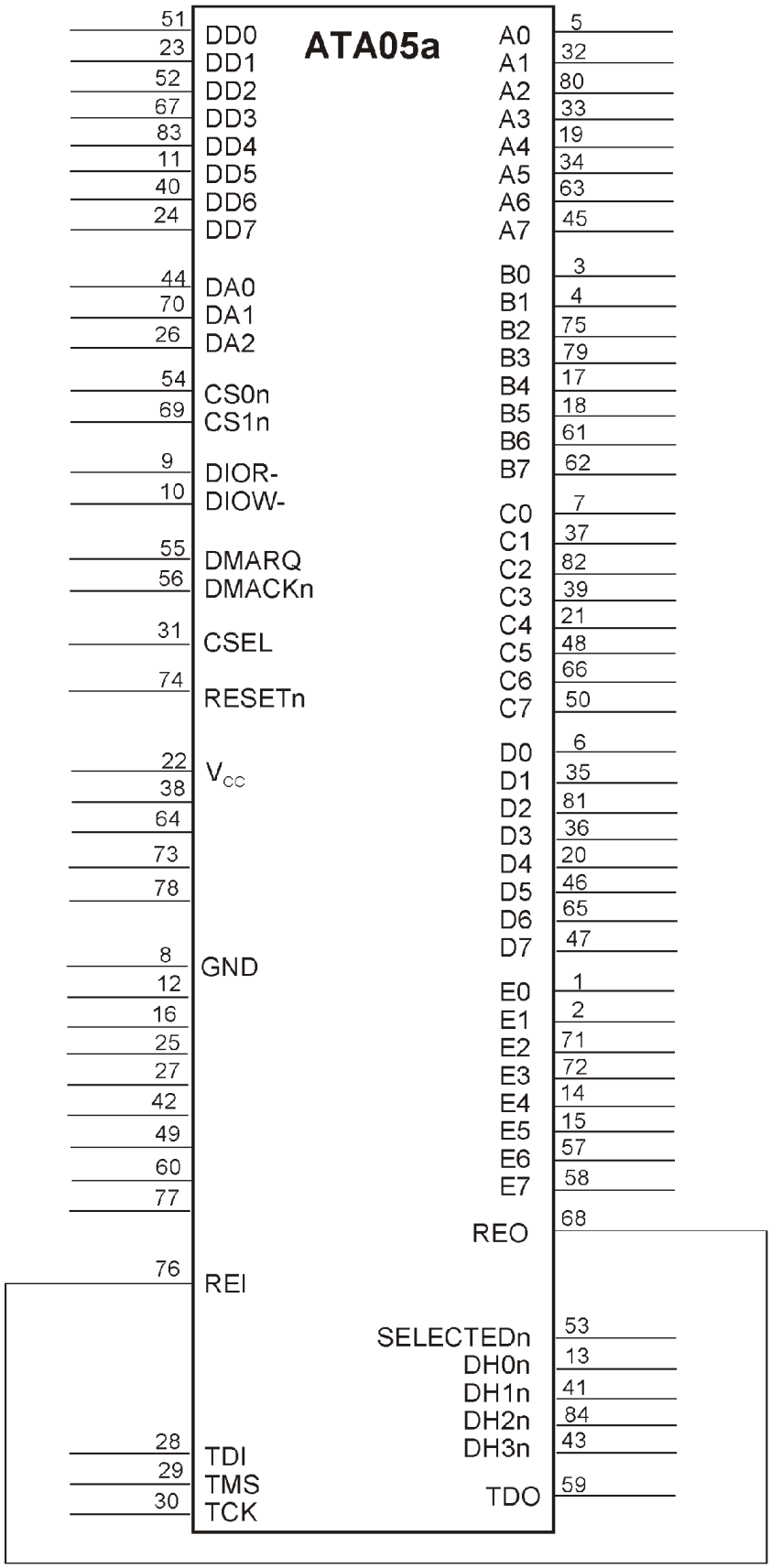
For more details see *http://www.controllersandpcs.de/ataio*.



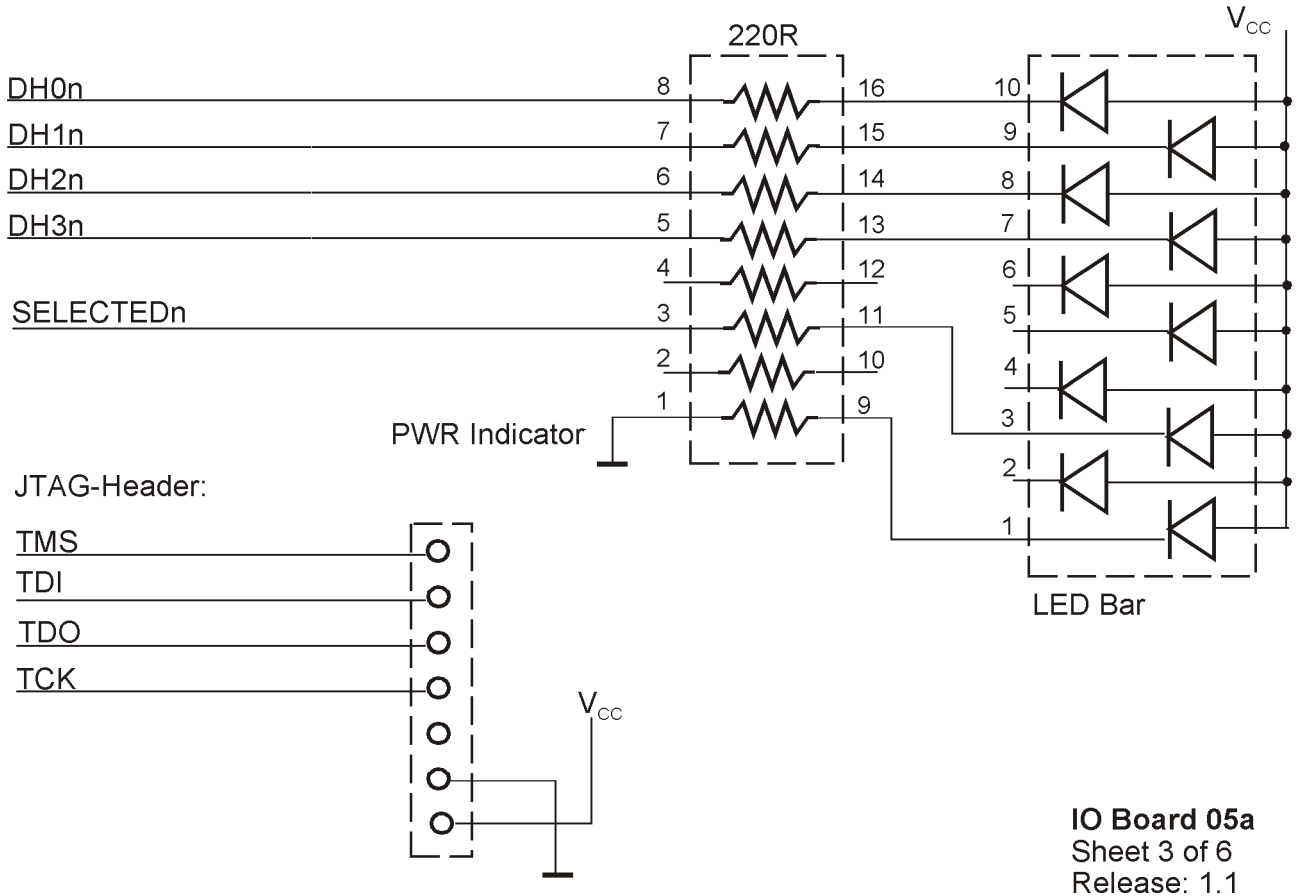


Jumper Settings:
a – Slave (Jumper in Parking Position)
b – Cable Select
c – Master

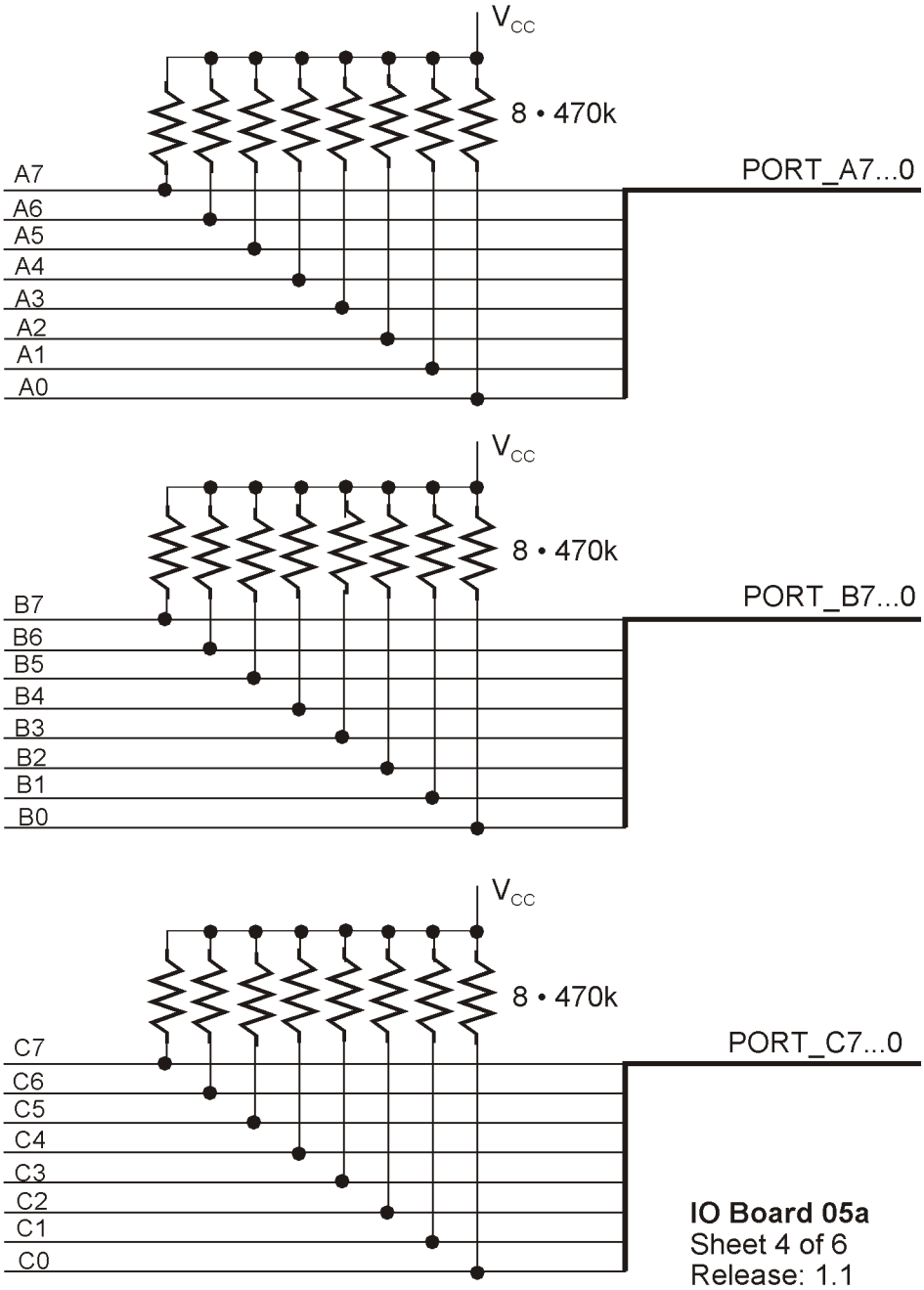
IO Board 05a
Sheet 1 of 6
Release: 1.1



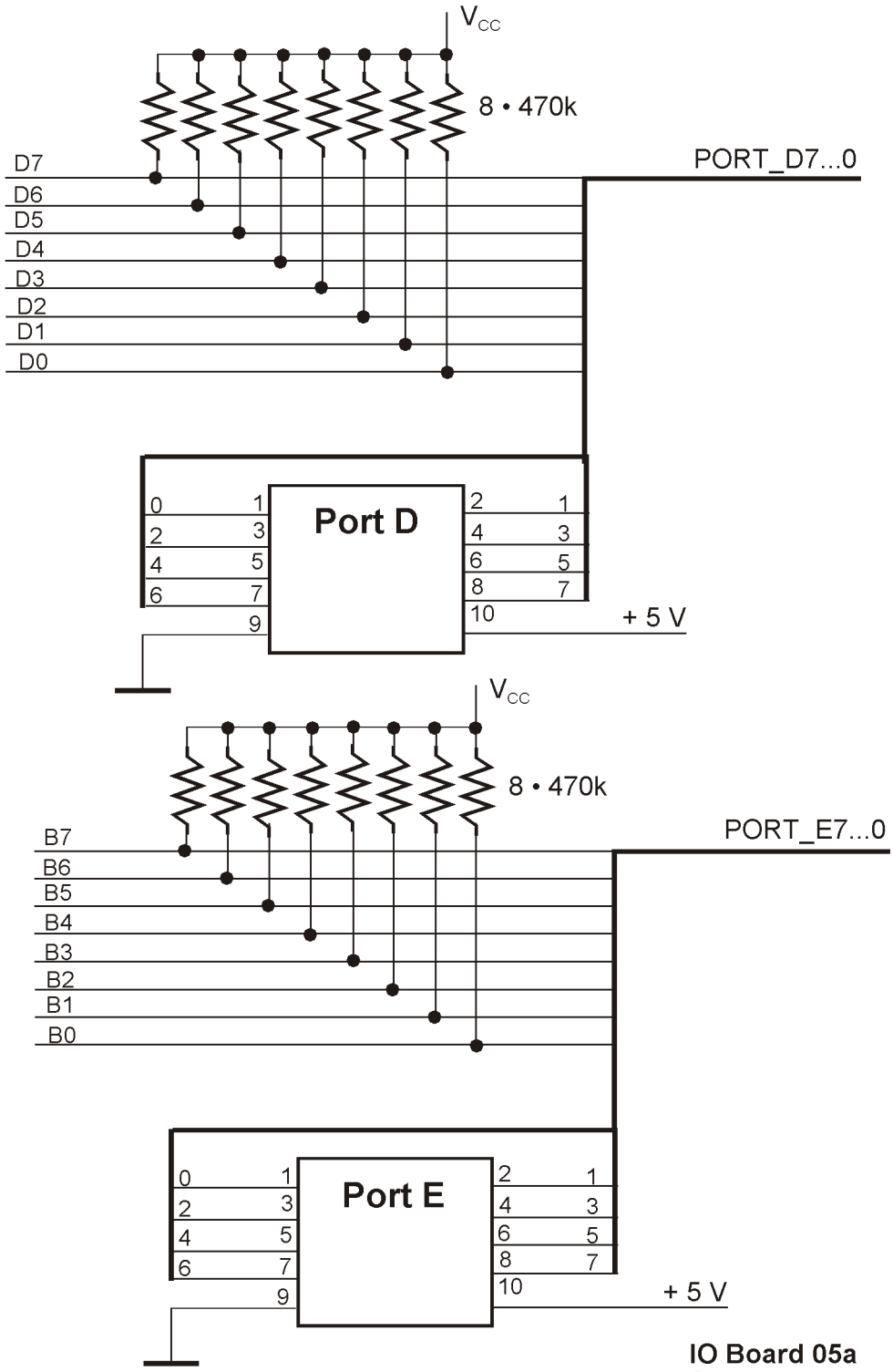
IO Board 05a
Sheet 2 of 6
Release: 1.1



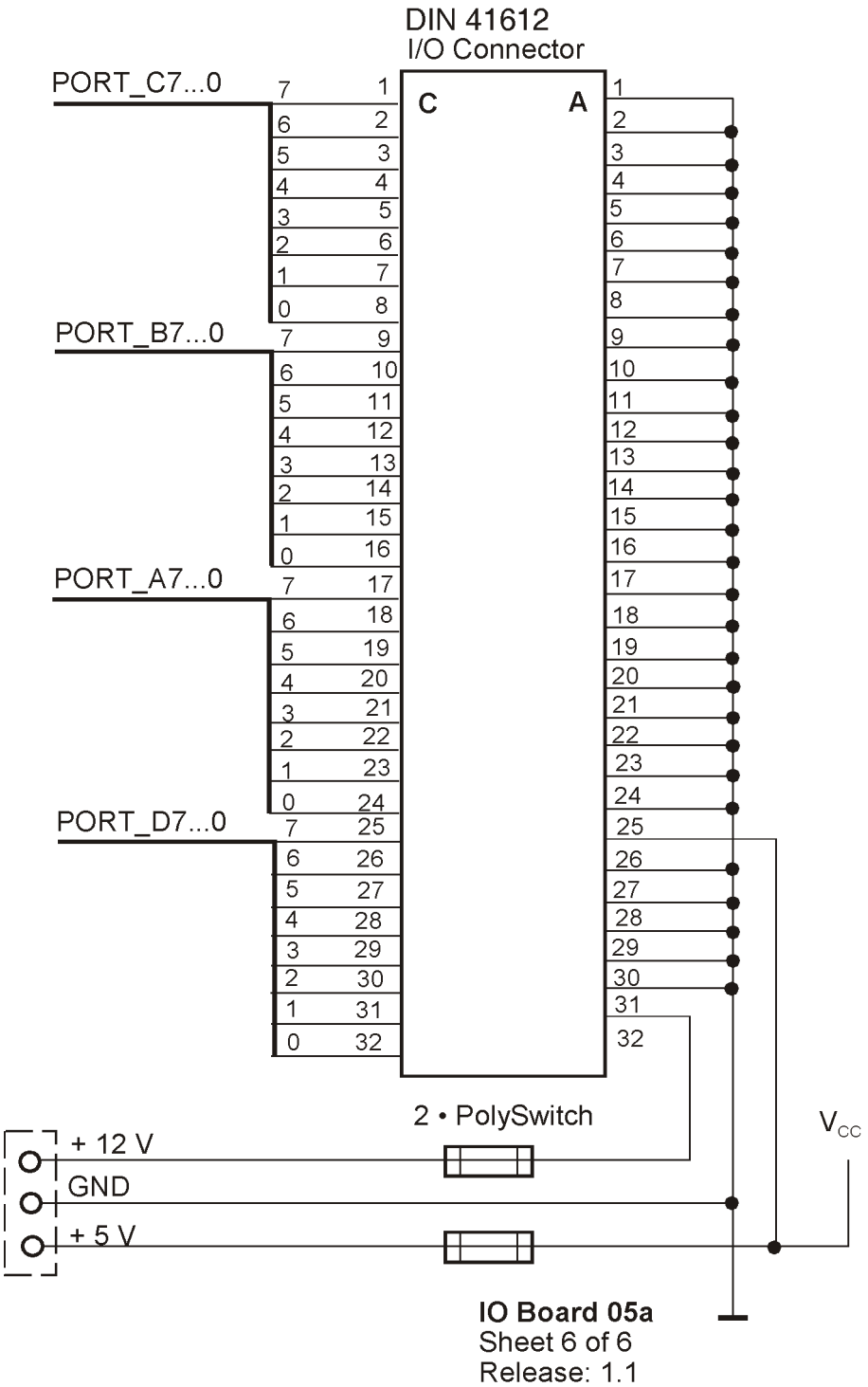
IO Board 05a
Sheet 3 of 6
Release: 1.1

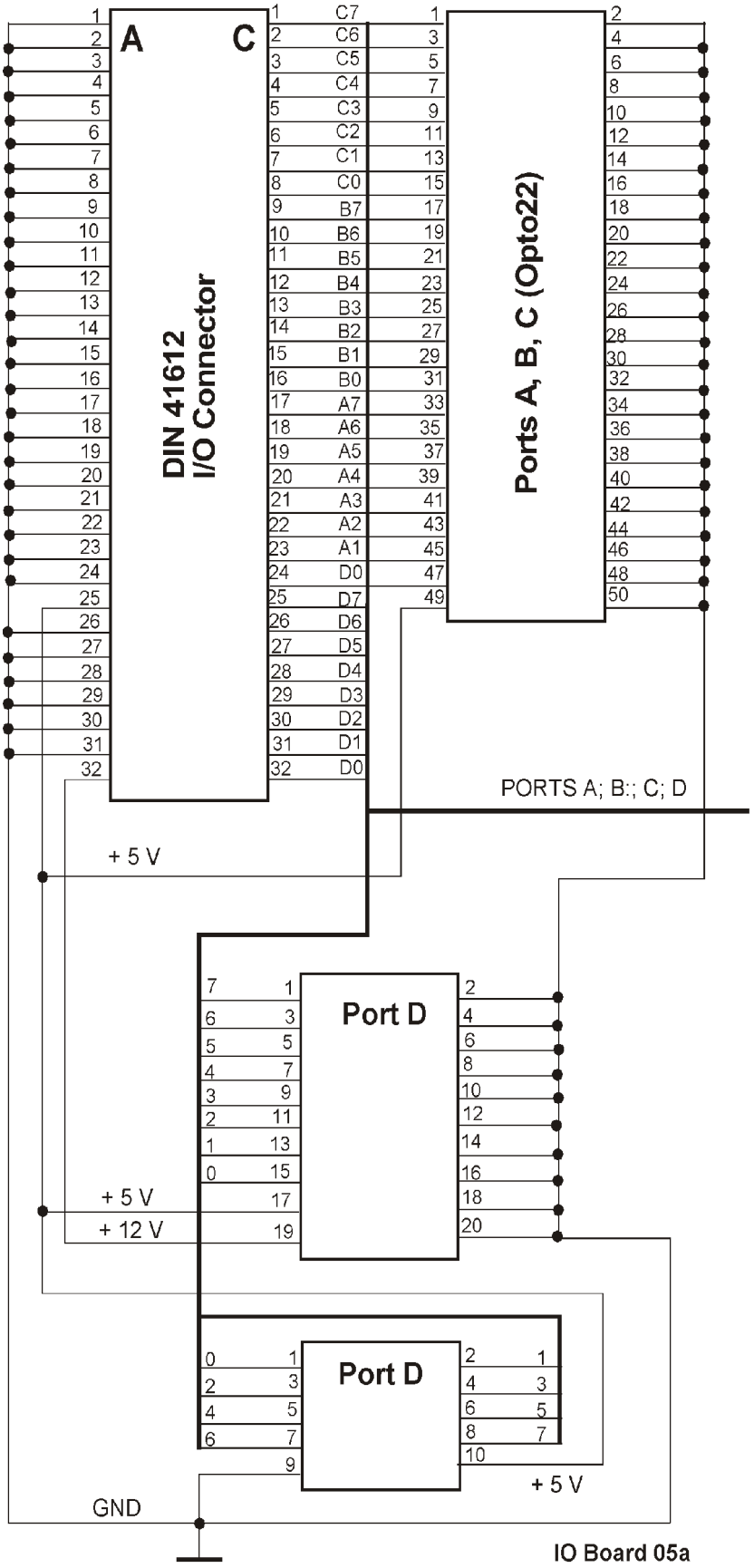


IO Board 05a
Sheet 4 of 6
Release: 1.1



IO Board 05a
Sheet 5 of 6
Release: 1.1


DIN 41612
I/O Connector
PORT_C7...0
PORT_B7...0
PORT_A7...0
PORT_D7...0
C
A
2 • PolySwitch
+ 12 V
GND
+ 5 V
V_{CC}
IO Board 05a
Sheet 6 of 6
Release: 1.1



IO Board 05a
Adapter card 05a1
Sheet 1 of 2
Release: 1.1