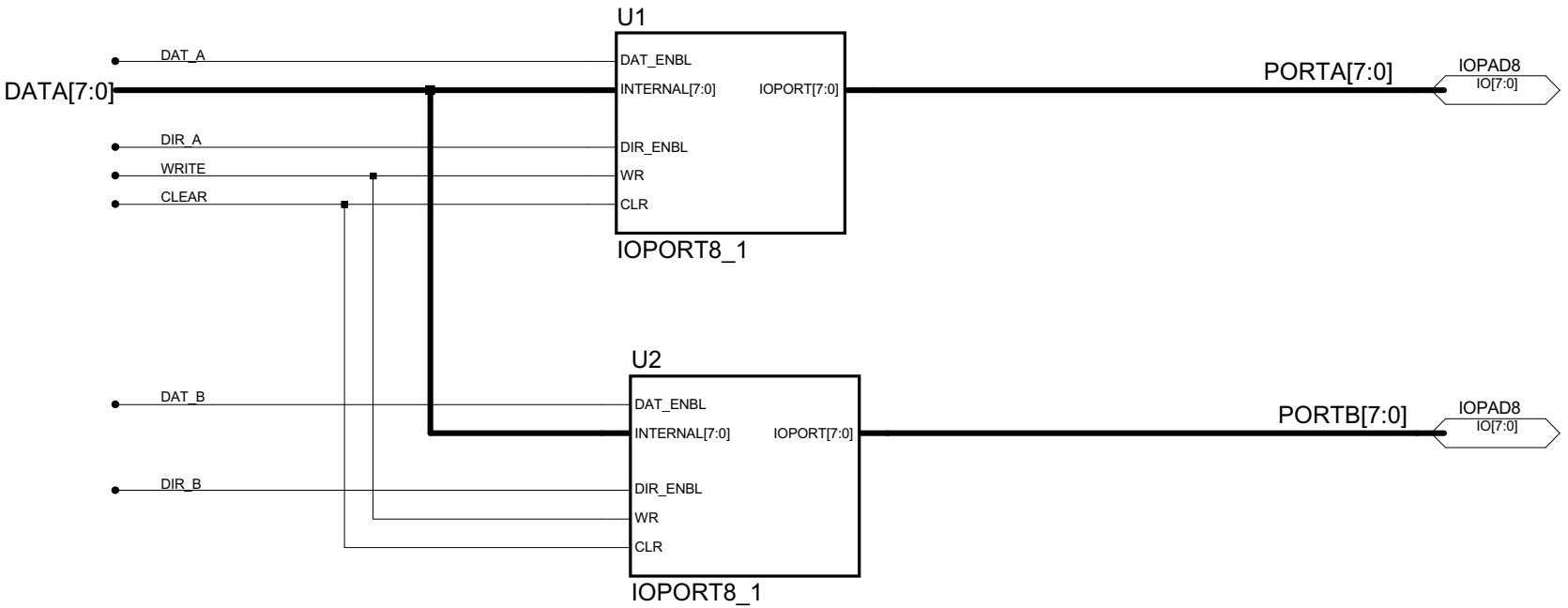
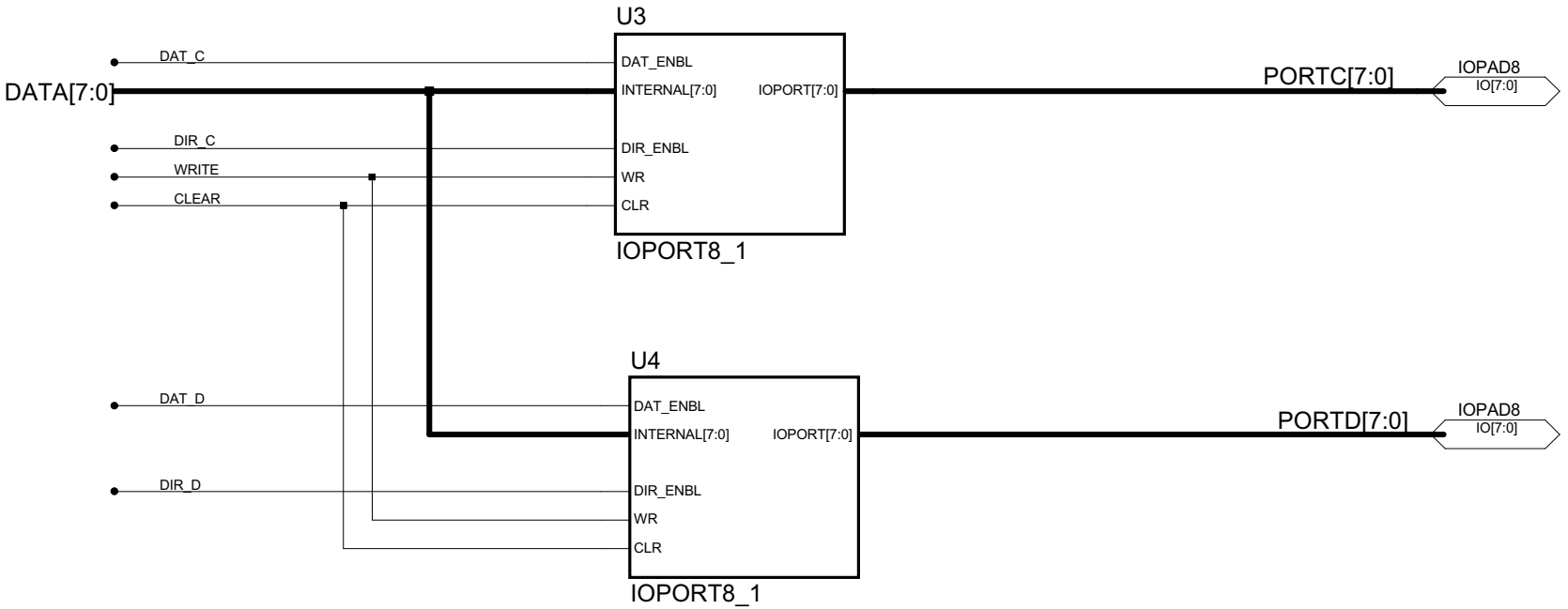


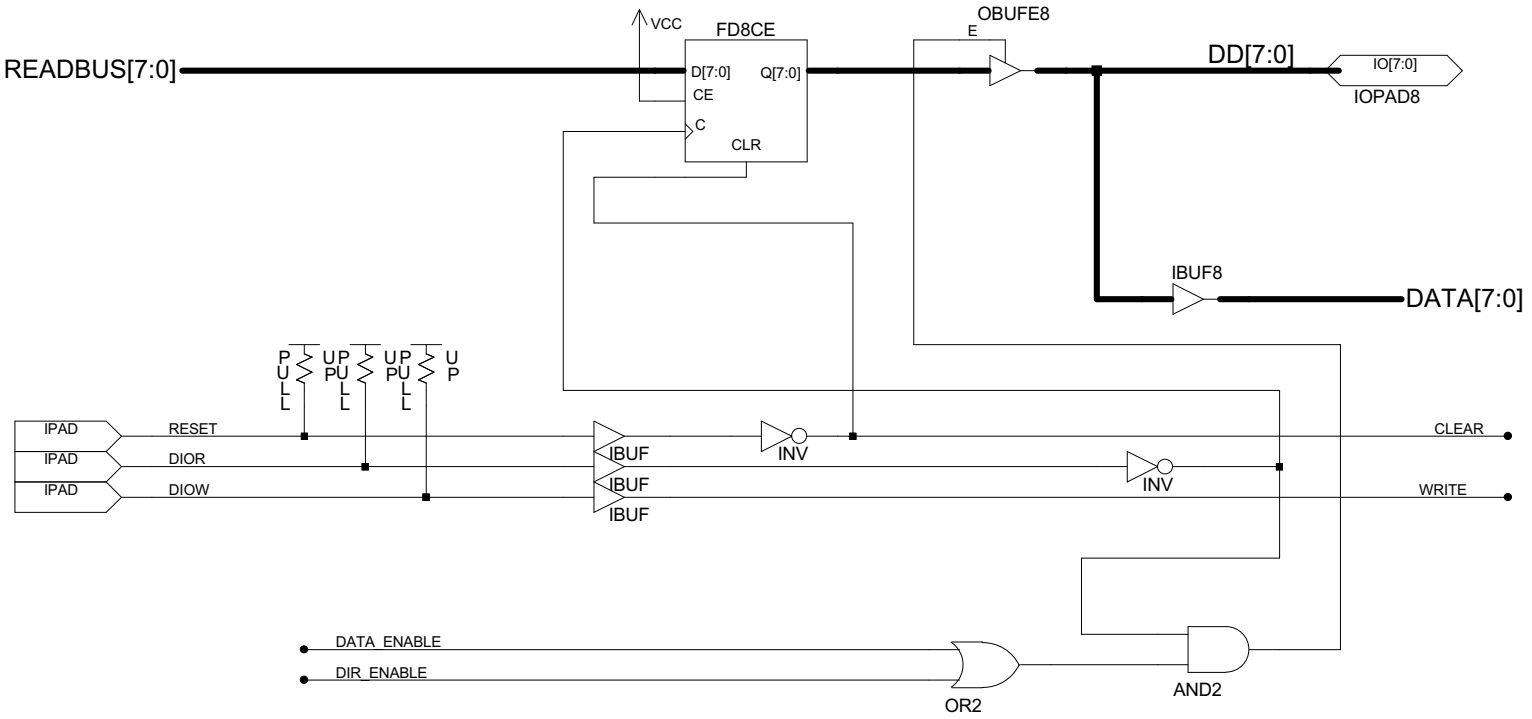
ATA port adapter 02f

IDE 32 + 8

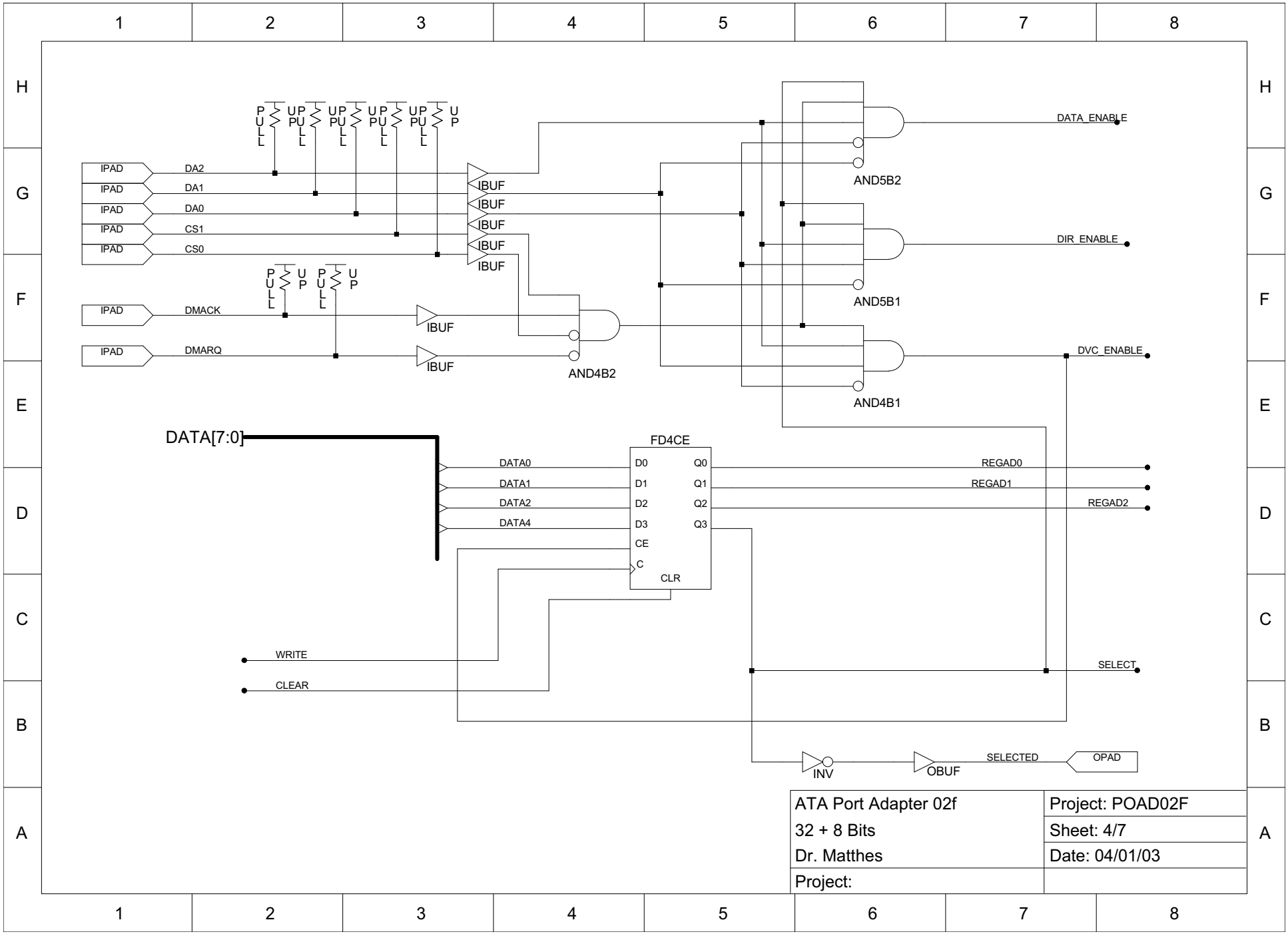
ATA Port Adapter 02f	Project: POAD02F
32 + 8 Bits	Sheet: 1/7
Dr. Matthes	Date: 01/07/03
Project: Portadapter 02f (IDE/32)	

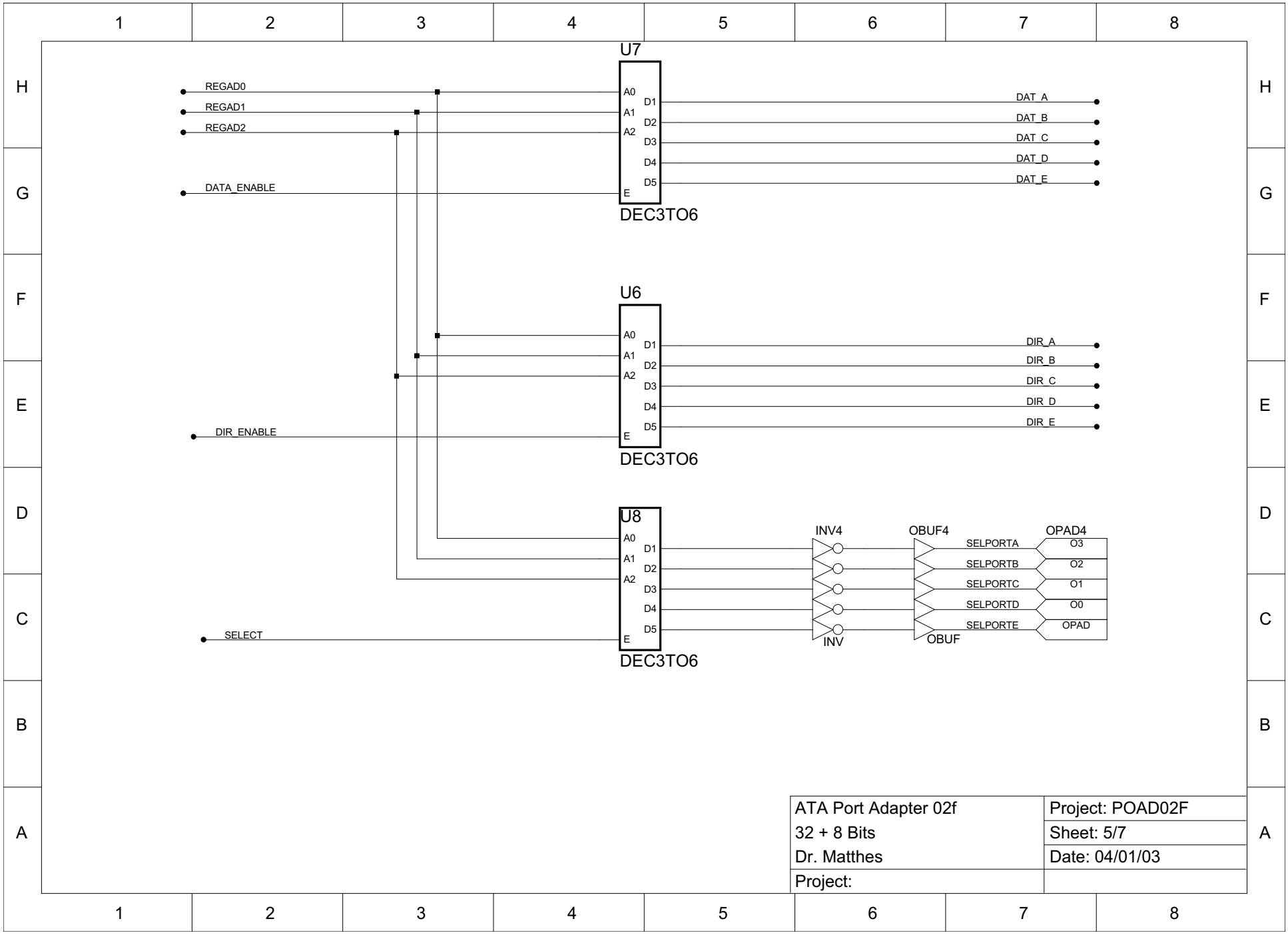


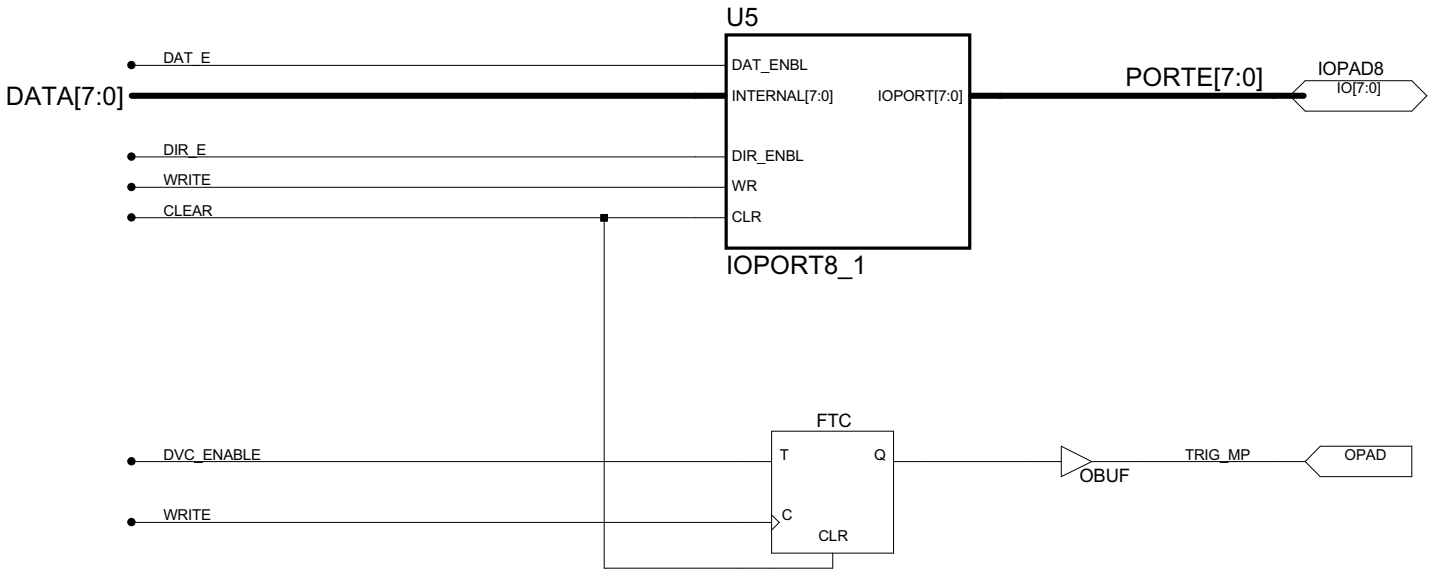
ATA Port Adapter 02f	Project: POAD02F
32 + 8 Bits	Sheet: 2/7
Dr. Matthes	Date: 04/01/03
Project:	



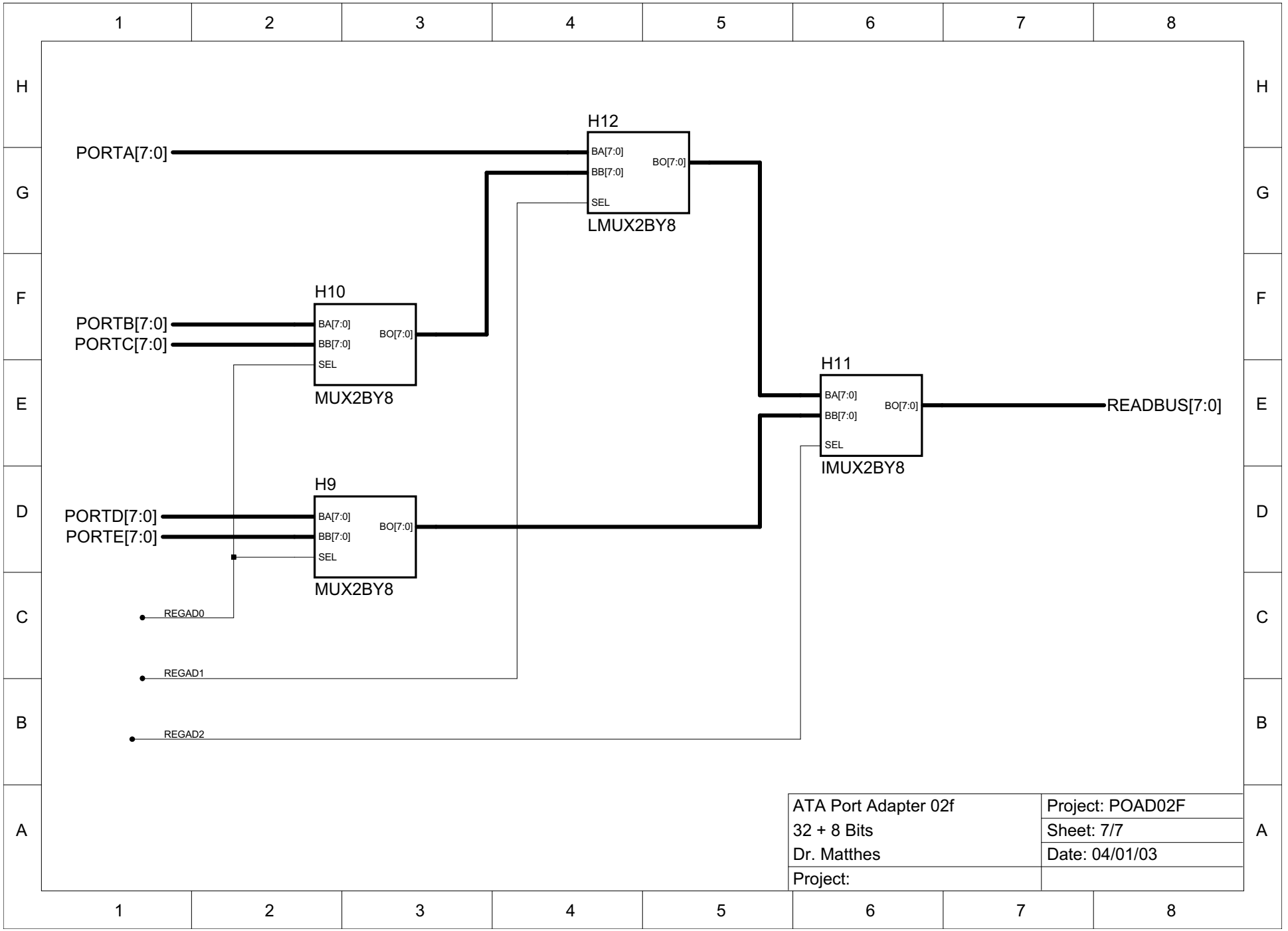
ATA Port Adapter 02f	Project: POAD02F
32 + 8 Bits	Sheet: 3/7
Dr. Matthes	Date: 04/01/03
Project:	


PULLUP
IPAD
DA2
DA1
DA0
CS1
CS0
IBUF
DMACK
DMARQ
AND4B2
AND5B2
AND5B1
AND4B1
DATA_ENABLE
DIR_ENABLE
DVC_ENABLE
DATA[7:0]
FD4CE
DATA0
DATA1
DATA2
DATA4
D0
D1
D2
D3
CE
C
CLR
Q0
Q1
Q2
Q3
REGAD0
REGAD1
REGAD2
WRITE
CLEAR
SELECT
INV
OBUF
SELECTED
OPAD
ATA Port Adapter 02f
32 + 8 Bits
Dr. Matthes
Project:
Project: POAD02F
Sheet: 4/7
Date: 04/01/03


U7
REGAD0
REGAD1
REGAD2
DATA_ENABLE
A0
A1
A2
E
D1
D2
D3
D4
D5
DAT_A
DAT_B
DAT_C
DAT_D
DAT_E
DEC3TO6
U6
DIR_ENABLE
DIR_A
DIR_B
DIR_C
DIR_D
DIR_E
DEC3TO6
U8
SELECT
INV4
OBUF4
OPAD4
SELPORTA
SELPORTB
SELPORTC
SELPORTD
SELPORTE
O3
O2
O1
O0
OPAD
INV
OBUF
DEC3TO6
ATA Port Adapter 02f
32 + 8 Bits
Dr. Matthes
Project:
Project: POAD02F
Sheet: 5/7
Date: 04/01/03



ATA Port Adapter 02f	Project: POAD02F
32 + 8 Bits	Sheet: 6/7
Dr. Matthes	Date: 04/01/03
Project:	


1
2
3
4
5
6
7
8
H
G
F
E
D
C
B
A
PORTA[7:0]
H12
BA[7:0]
BB[7:0]
SEL
BO[7:0]
LMUX2BY8
PORTB[7:0]
PORTC[7:0]
H10
BA[7:0]
BB[7:0]
SEL
BO[7:0]
MUX2BY8
H11
BA[7:0]
BB[7:0]
SEL
BO[7:0]
IMUX2BY8
READBUS[7:0]
PORTD[7:0]
PORTE[7:0]
H9
BA[7:0]
BB[7:0]
SEL
BO[7:0]
MUX2BY8
REGAD0
REGAD1
REGAD2
ATA Port Adapter 02f
32 + 8 Bits
Dr. Matthes
Project:
Project: POAD02F
Sheet: 7/7
Date: 04/01/03