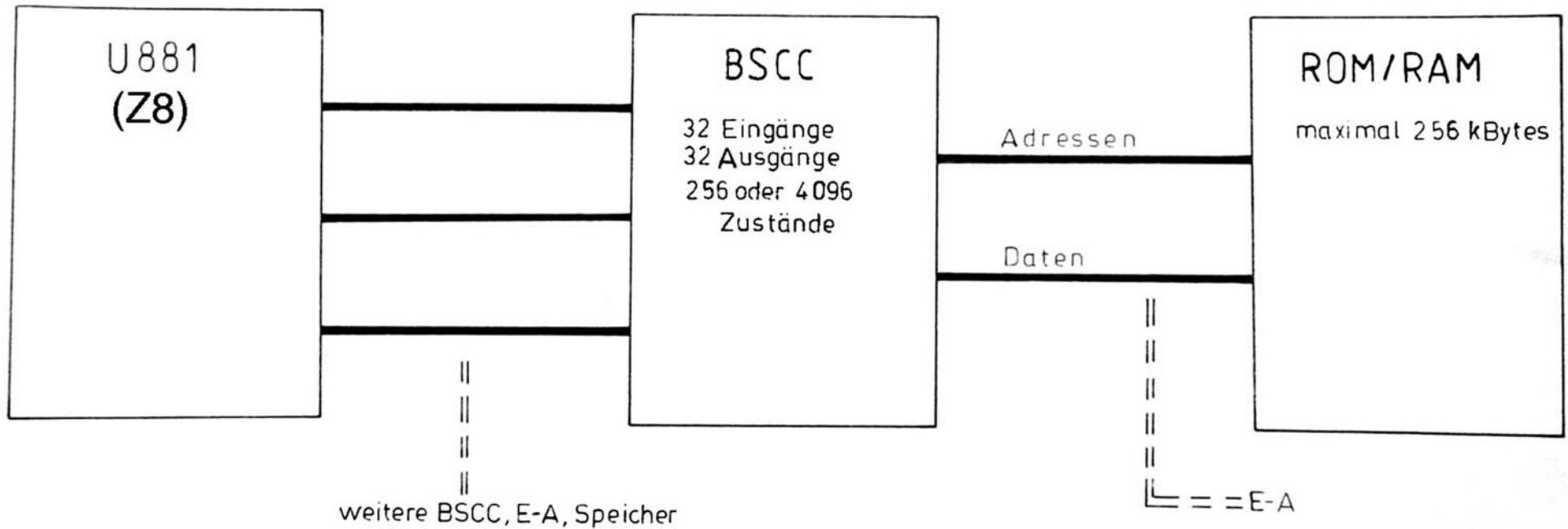


BSCC

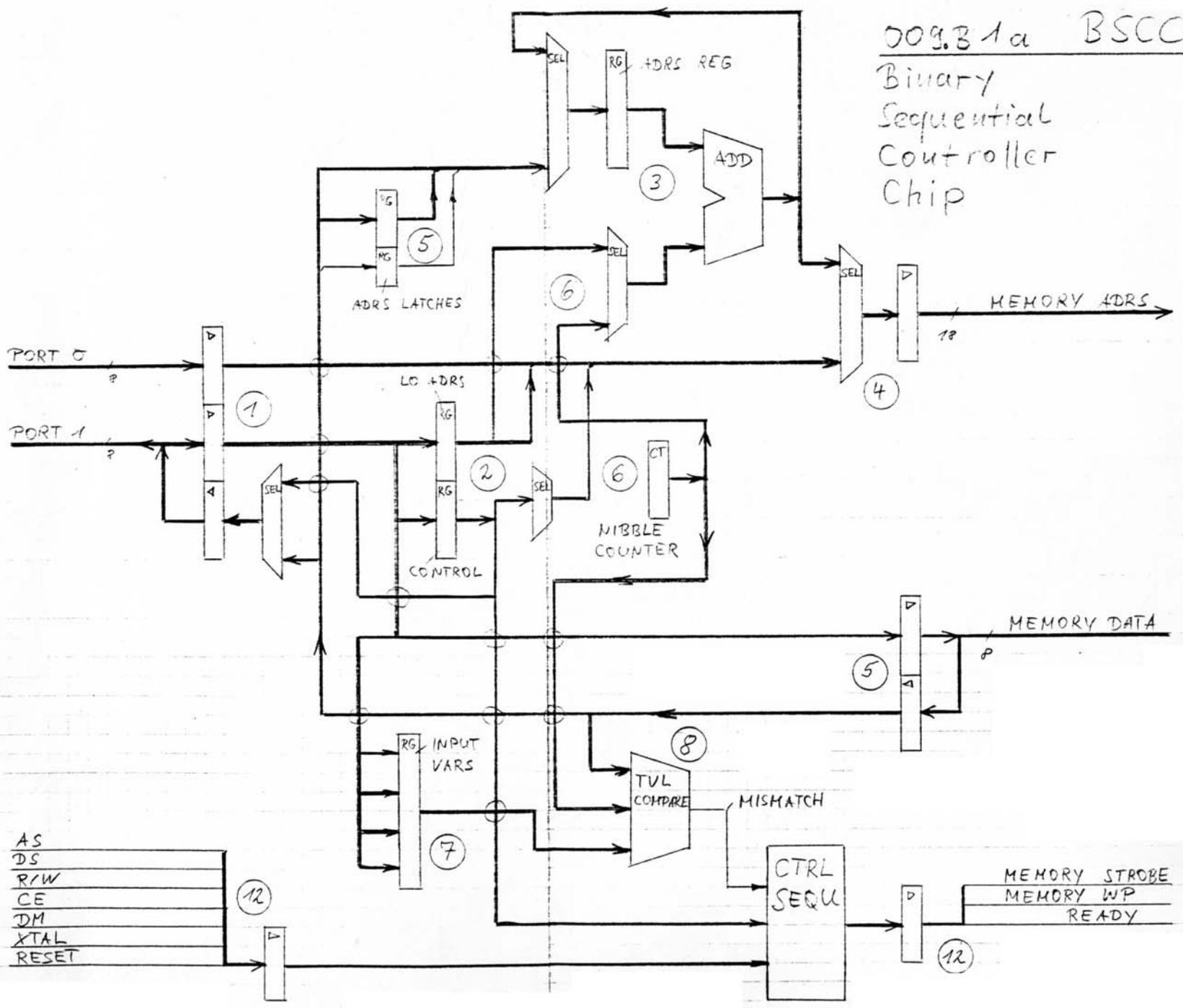
Binary
Sequential
Controller
Chip

Binärsteuerungen mit Einchipmikrorechner



Microcomputer

8

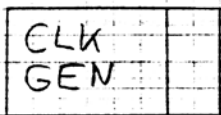
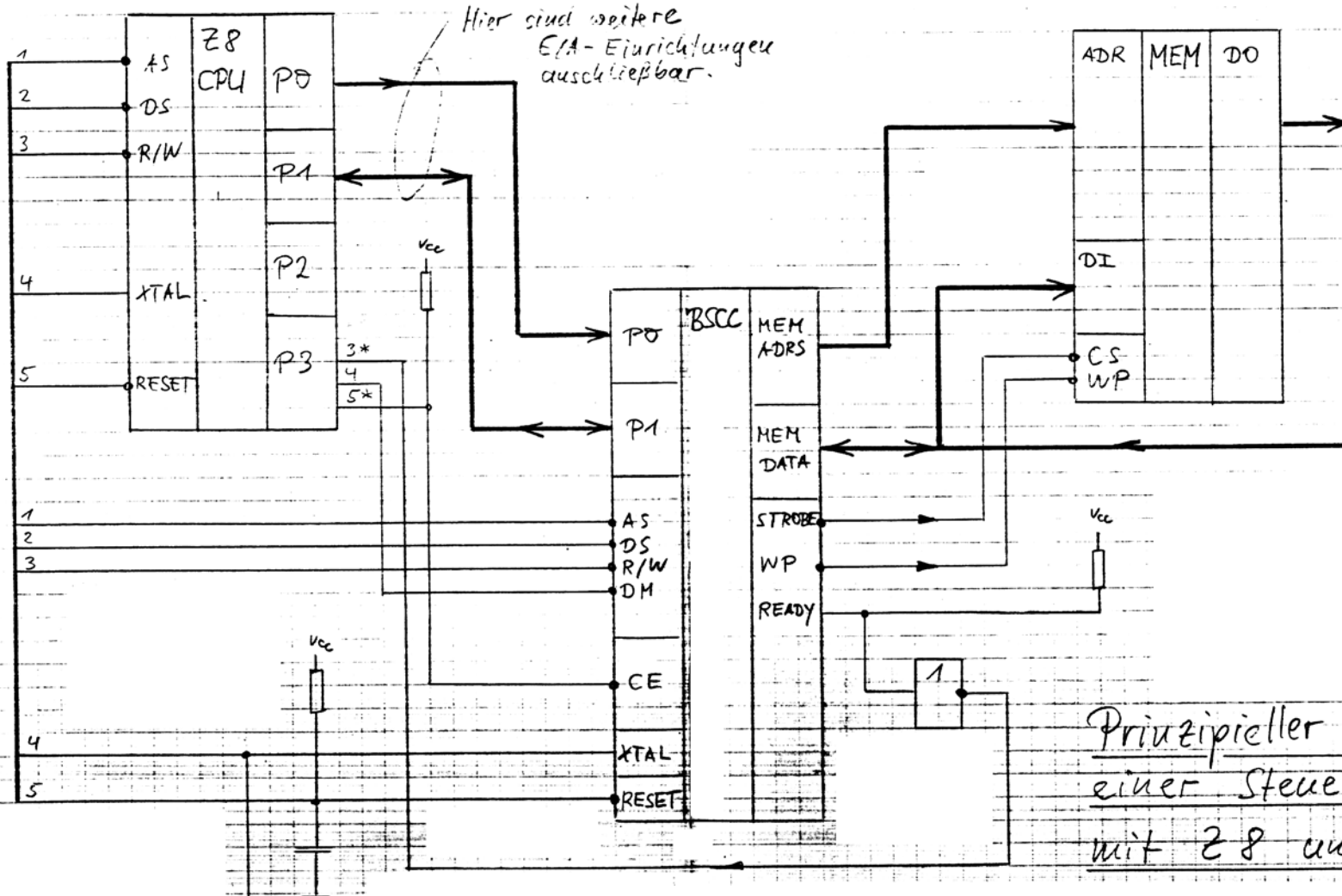


009.B1a BSCC
 Binary
 Sequential
 Controller
 Chip

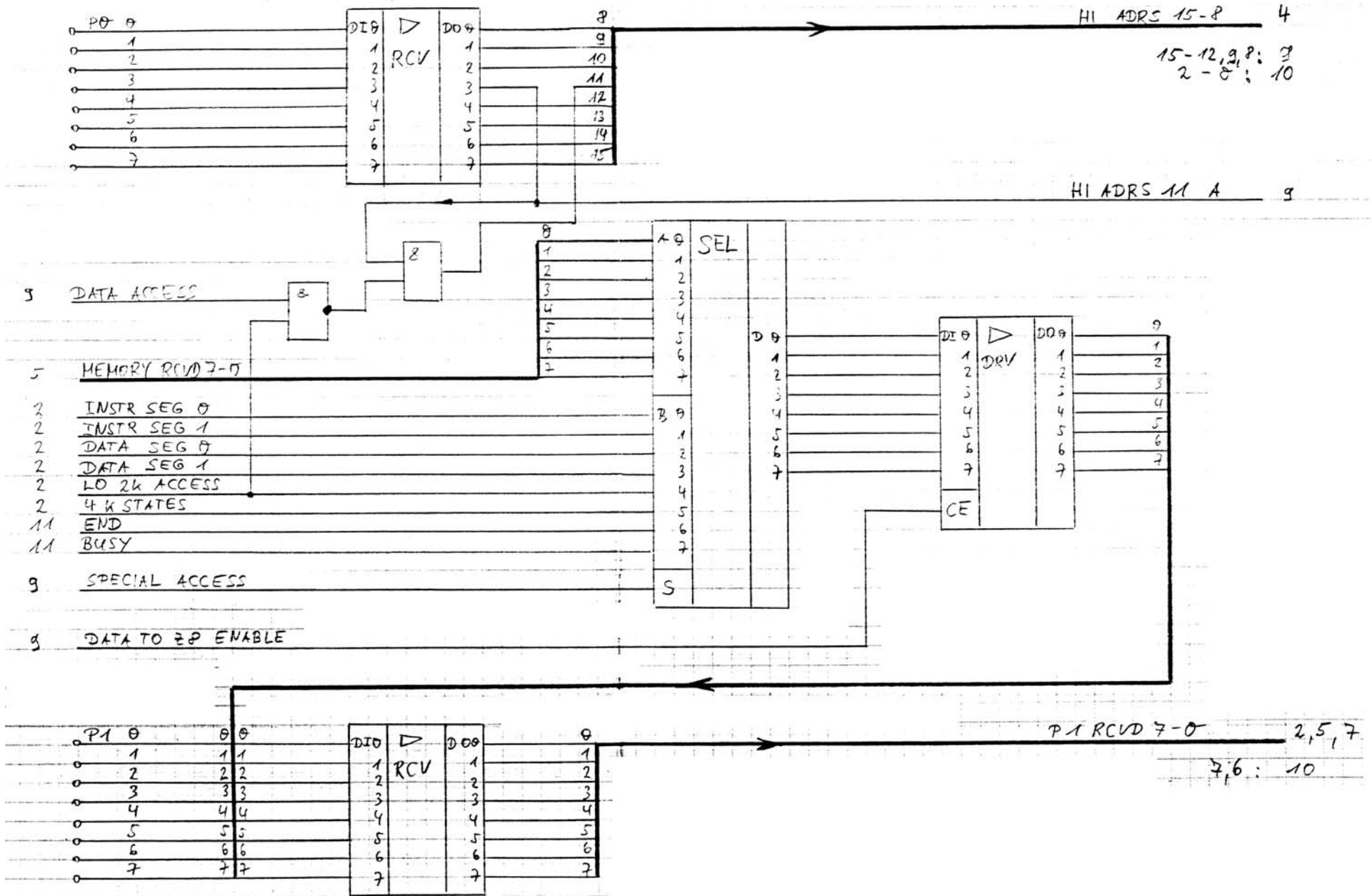
MEMORY

AS
 DS
 R/W
 CE
 DM
 XTAL
 RESET

MEMORY STROBE
 MEMORY WP
 READY

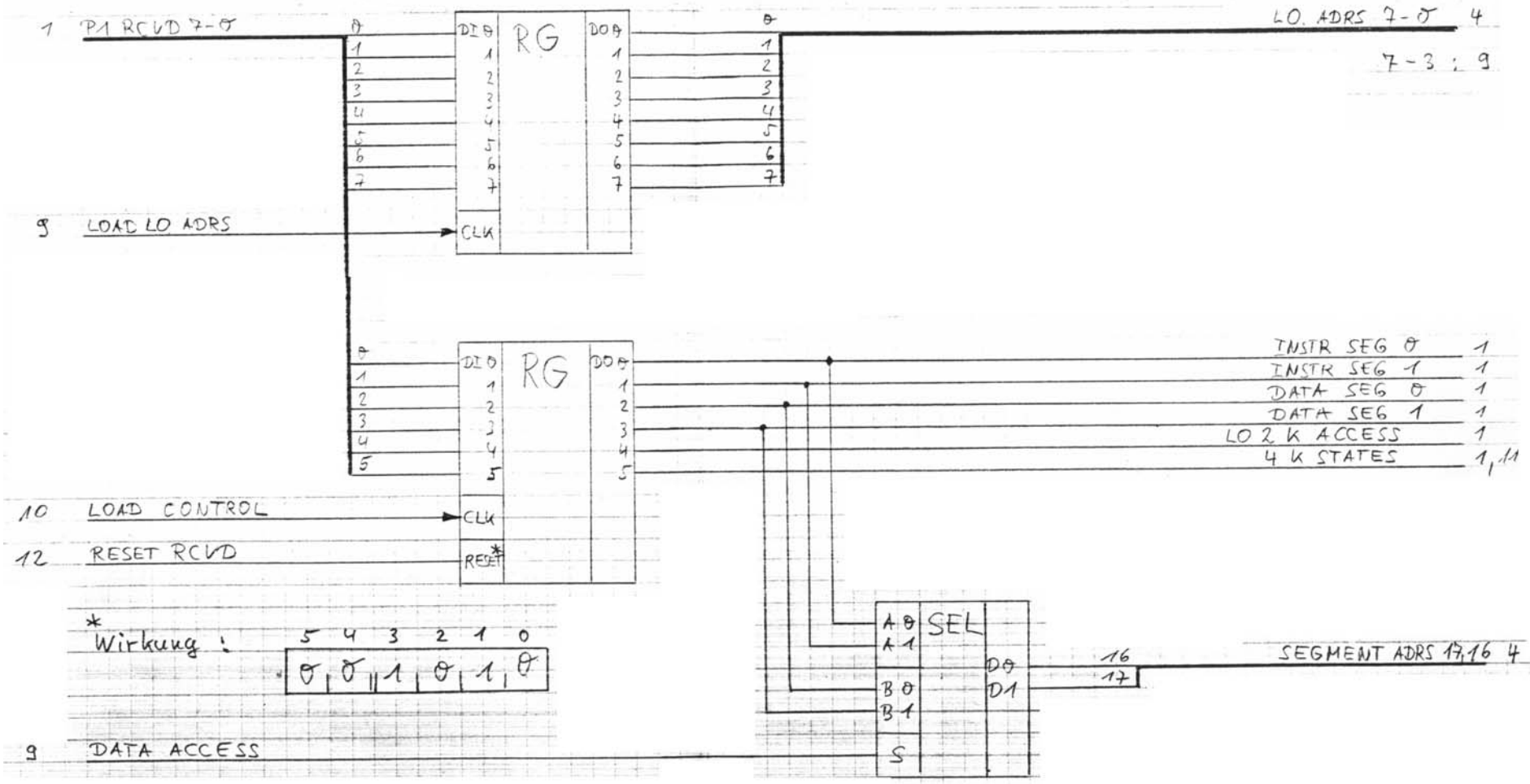


* Empfohlene Belegung (läßt Bits 0, 1, 6, 7 frei für CTC u. SIO-Funktionen; belegt nur eine der IRQ-Positionen)



Port-Kopplung Z8-seitig ①

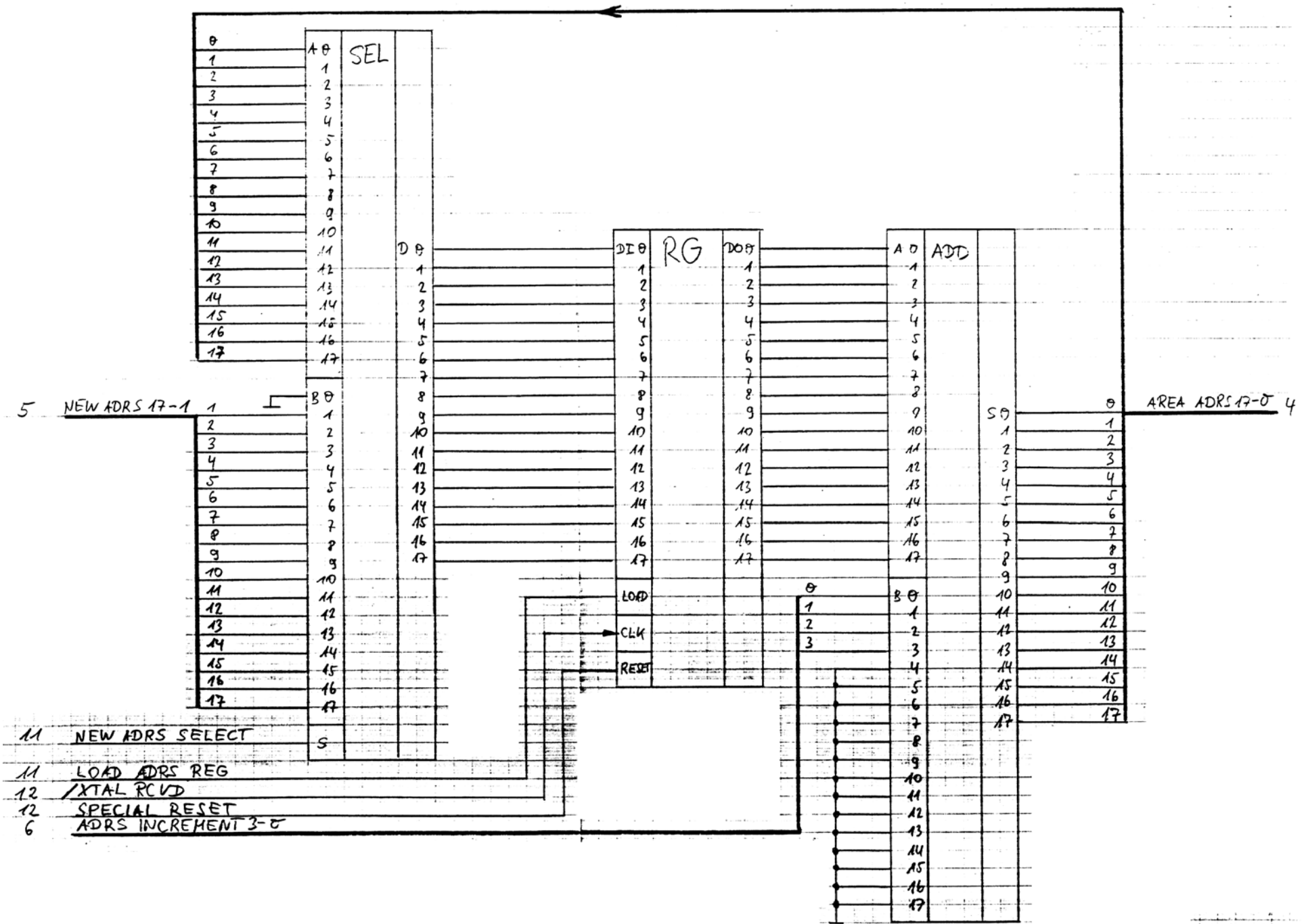
1.9.87 lla



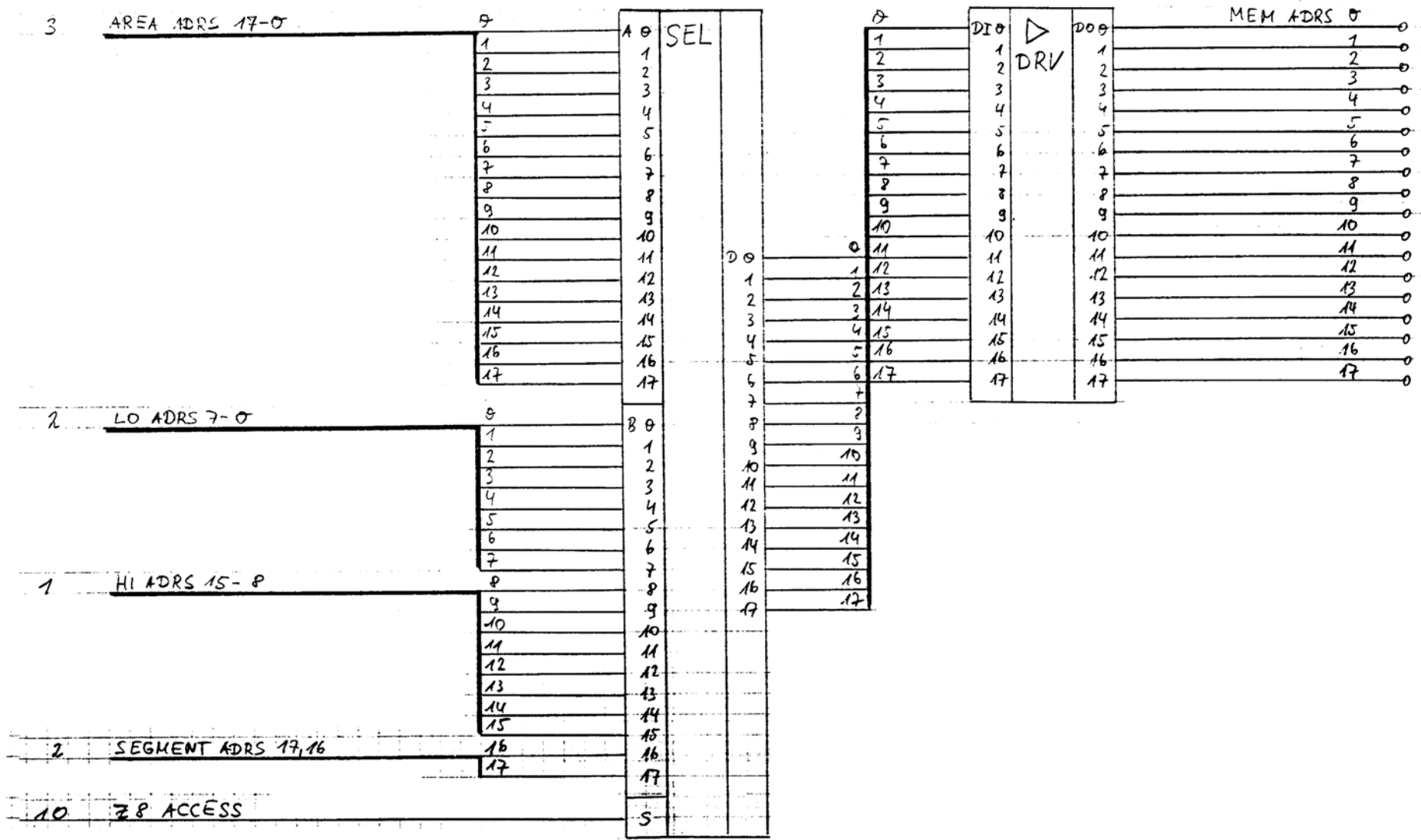
Adressenhalterregister, CONTROL-Register

1.9.82 Ma

2



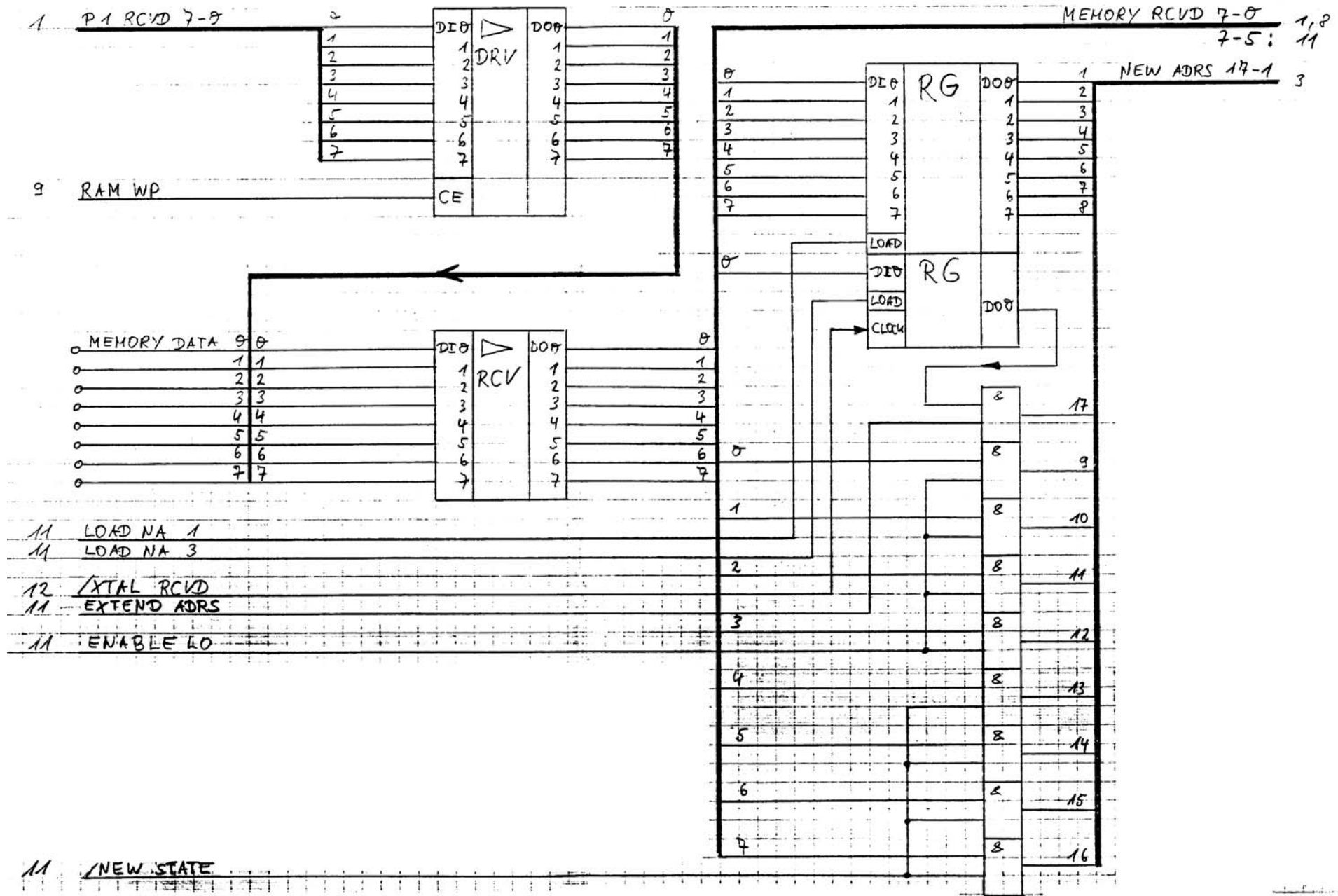
Adressen-Addierwerk



Kopplung Speicheradresse

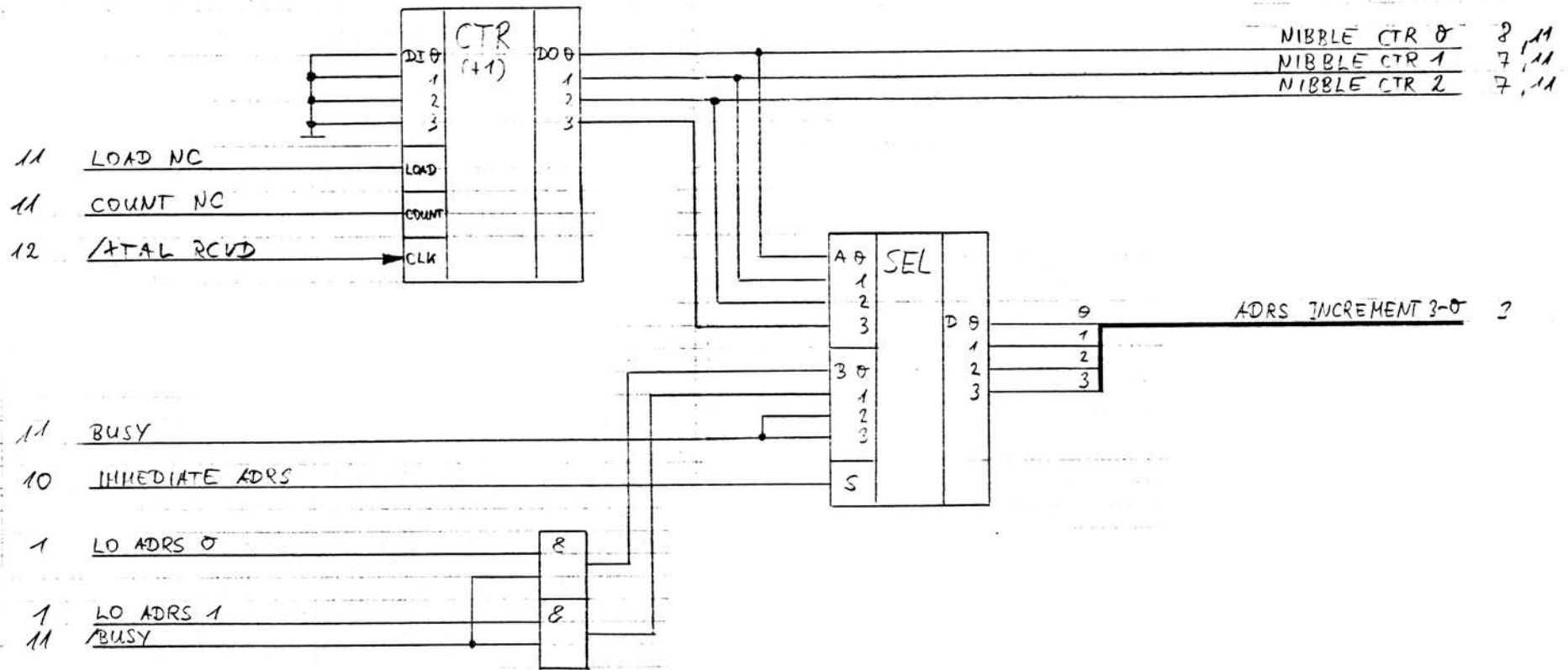
4

1.9.27 illa



Kopplung Speicherdaten, Adressen - Vorregister 5

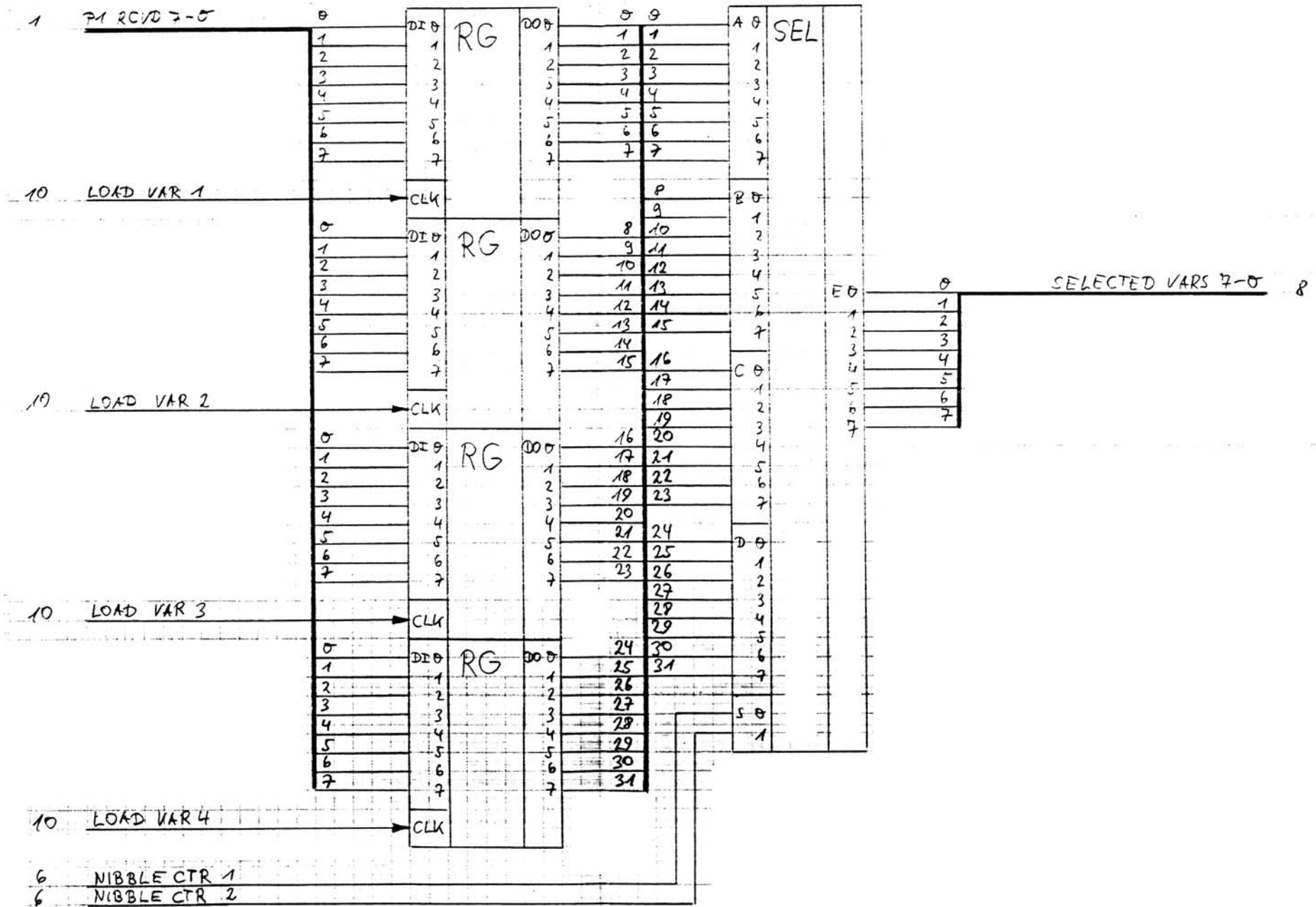
1.9.87



Abschnittszähler mit Auswahl

6

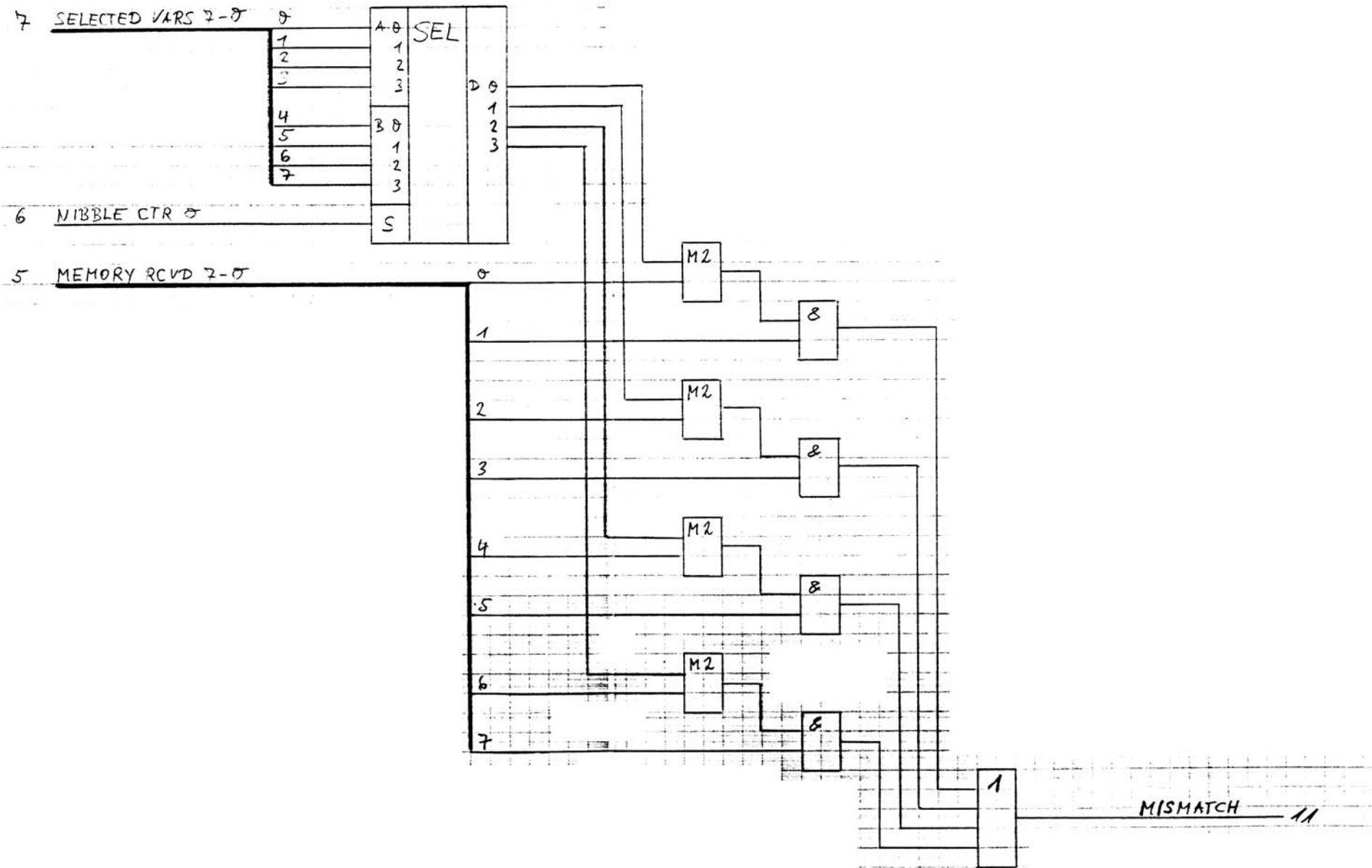
1.9.87 lla



Variableuregister mit Vorauswahl

17

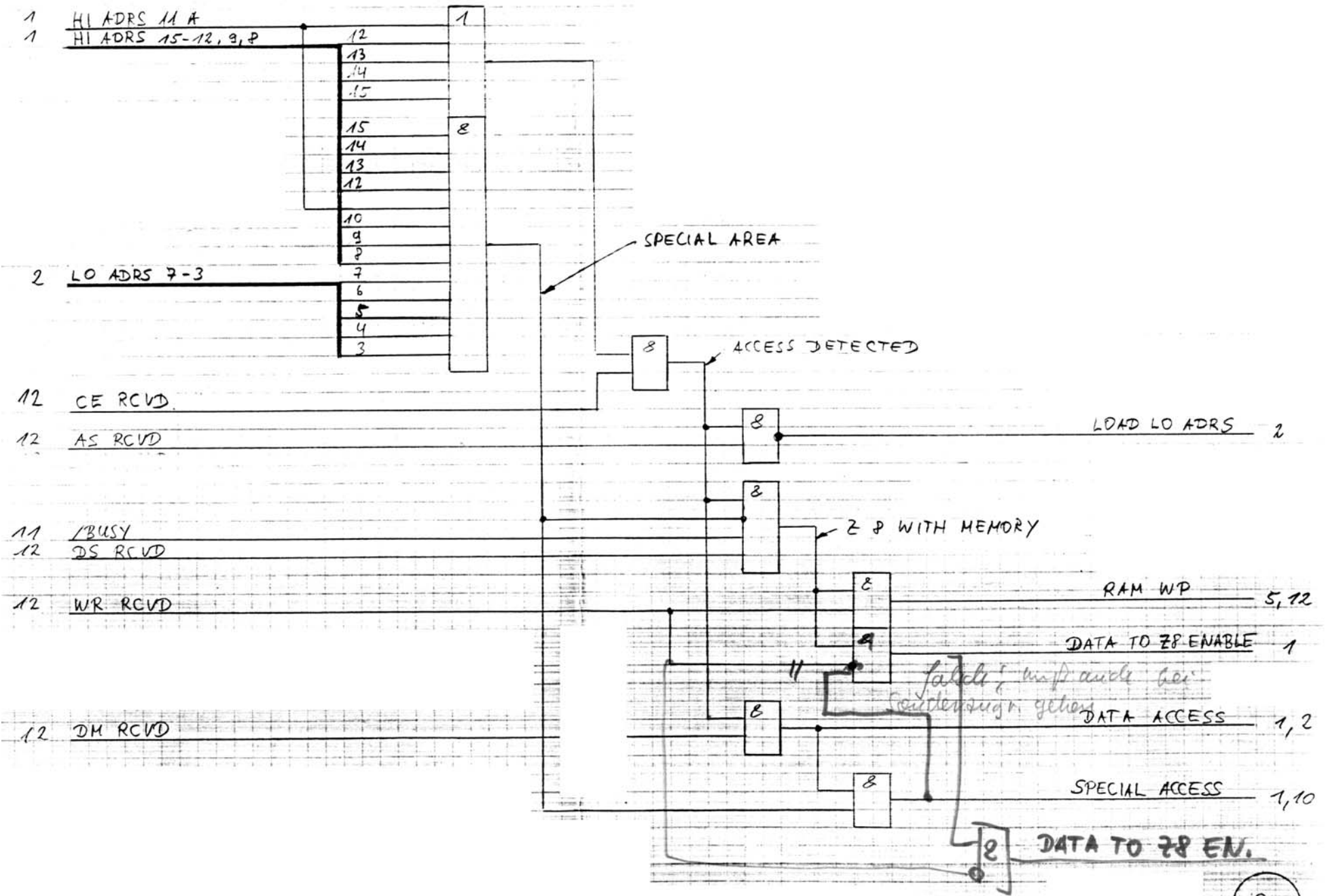
1.9.87 lla

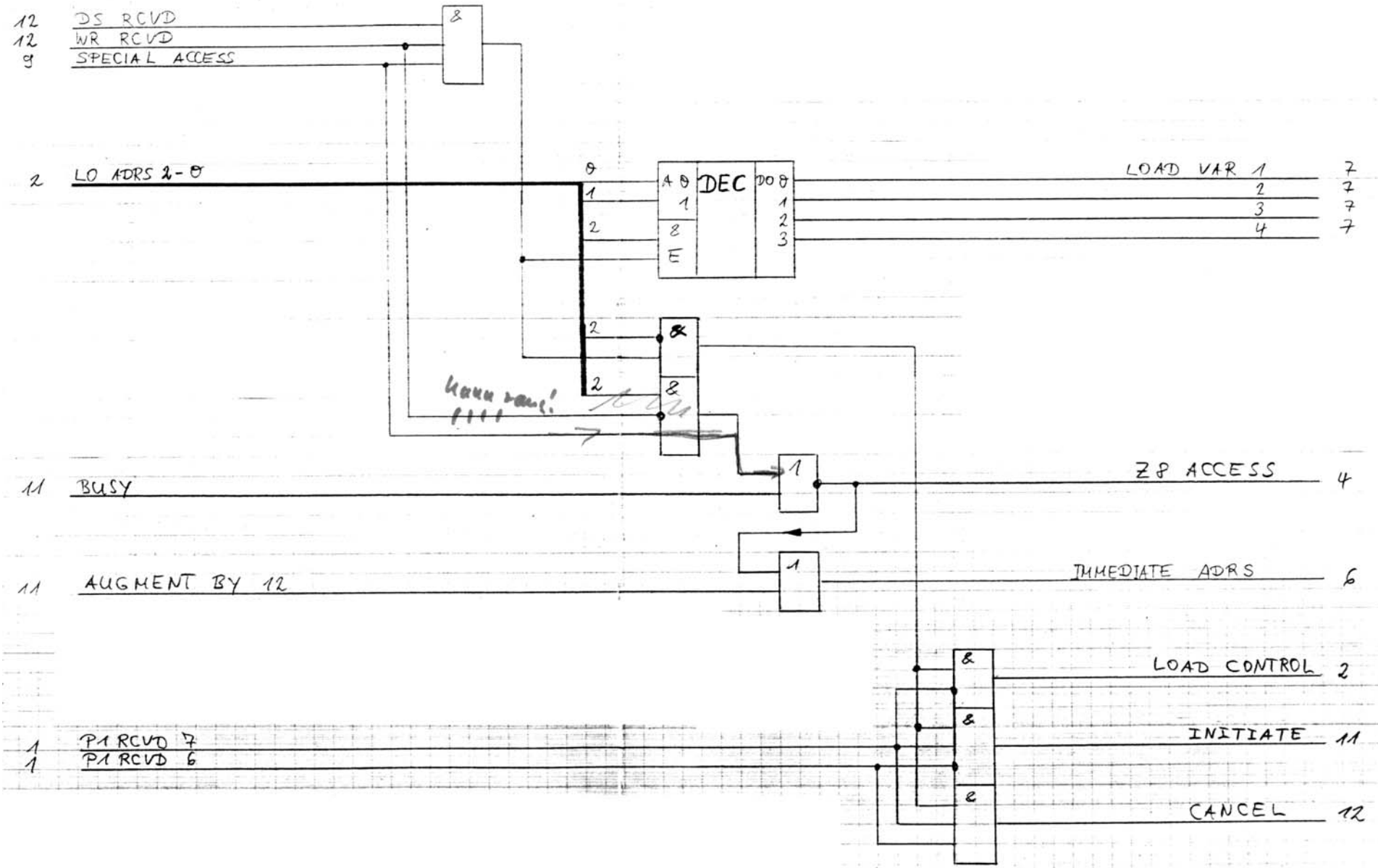


TVL-Vergleich

8

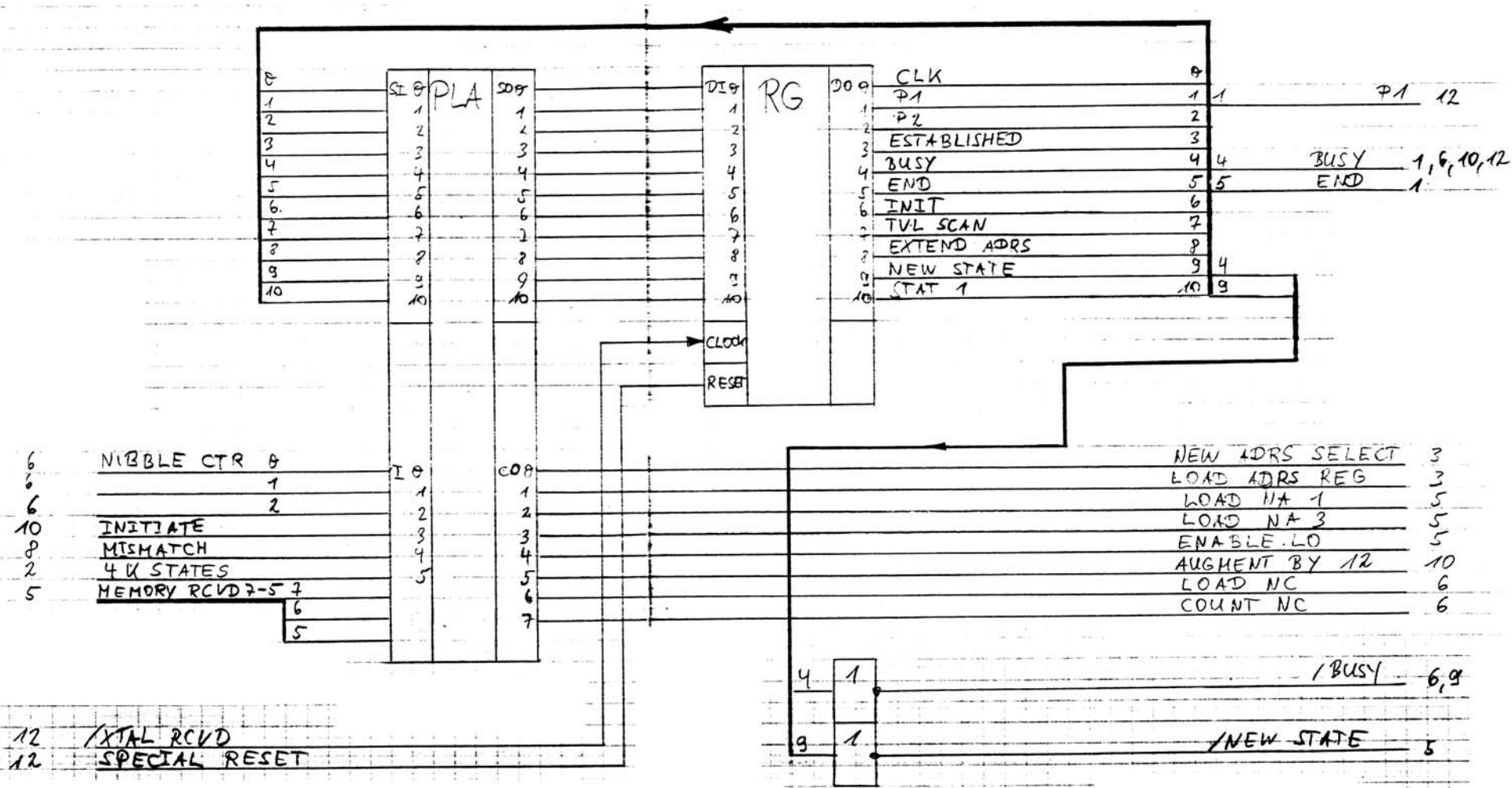
1.8.27.11





Zugriffs-Steuerung II

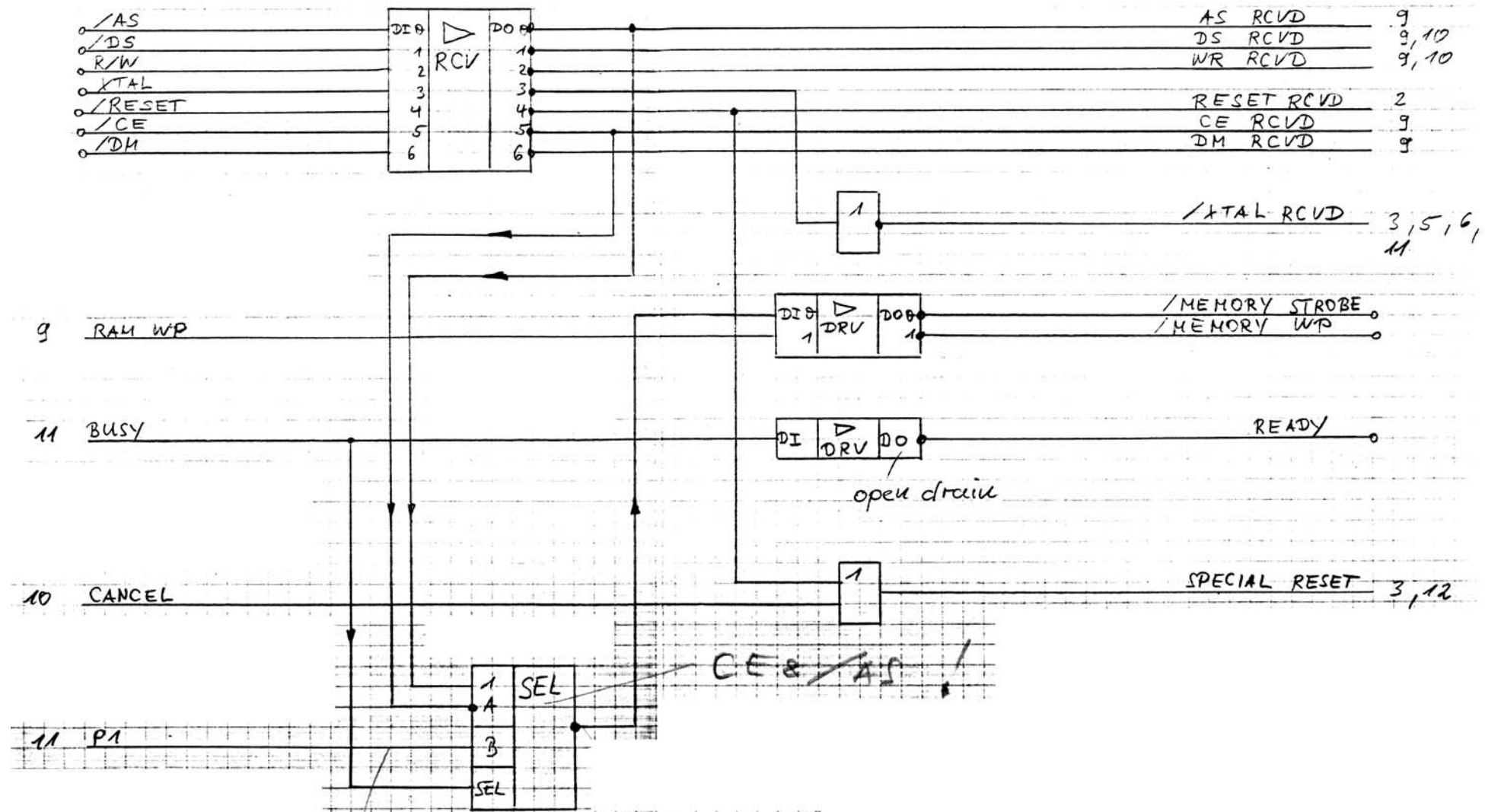
1.3.87 sll



Sequencer

11

2.P.9 P9, Ua

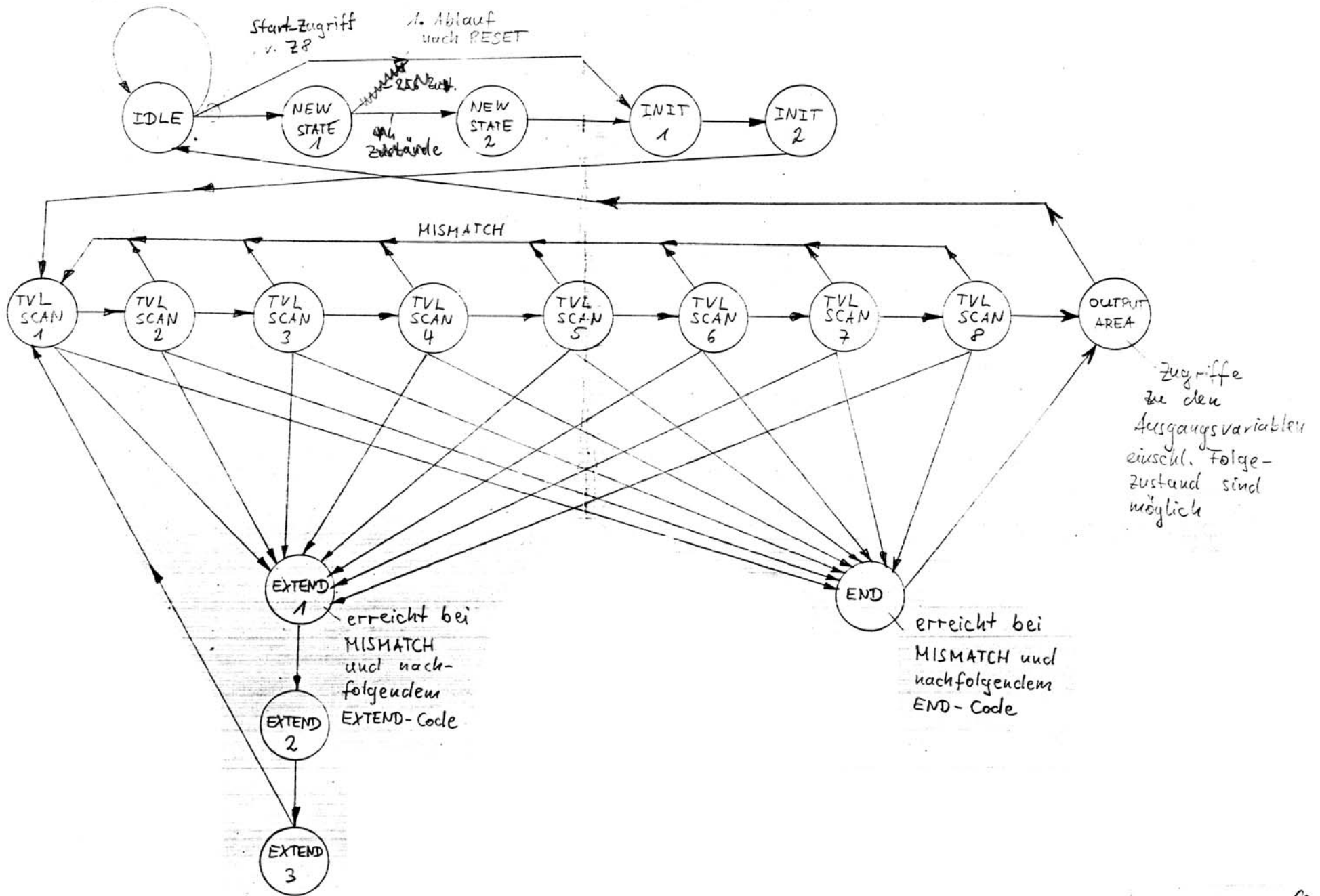


Ausstattung bei CLOCK & P1!

Kopplung Steuerleitungen

(12)

1.9.87-llg



22.1.87 ille