

ATmega1284							
Freely available	PA0	40	PA0 SCL PC0	22	(I2C)**		
	PA1	39	PA1 SDA PC1	23			
	PA2	38	PA2 TCK PC2	24			
	PA3	37	PA3 TMS PC3	25	Freely available		
	PA4	36	PA4 TDO PC4	26			
	PA5	35	PA5 TDI PC5	27			
	PA6	34	PA6 TOSC1 PC6	28			
	PA7	33	PA7 TOSC2 PC7	29			
SPI / programmer freely available.	EPDL SS#	1	PB0 XCK/T0	RX0 PD0	14	RX	Serial communication
	EPDR SS#	2	PB1 T1	TX0 PD1	15	TX	
	DISCHARGE	3	PB2 INT2	RX1 INT0 PD2	16		PWR_ON
	BORDER*	4	PB3 OC0	TX1 INT1 PD3	17		SSW1
	RESET	5	PB4 SS#	OC1B PD4	18		SSW2
	MOSI	6	PB5 MOSI	OC1A PD5	19		EPD_PWM*
	MISO	7	PB6 MISO	ICP1 PD6	20		BUSYL
	SCK	8	PB7 SCK	OC2 PD7	21		BUSYR

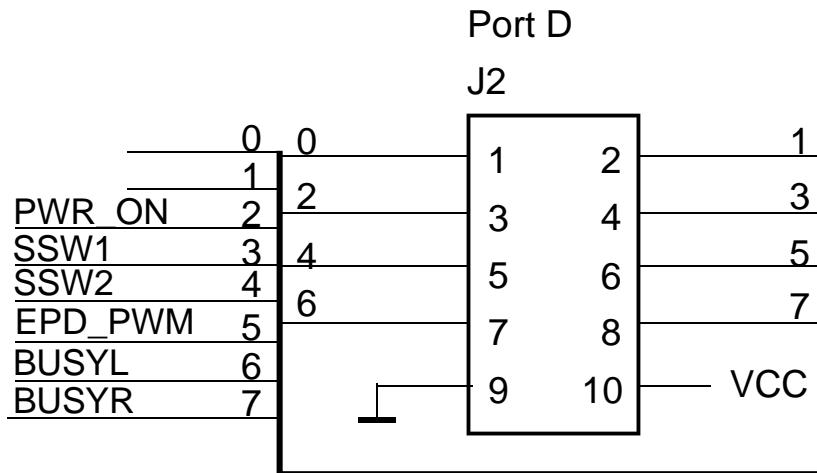
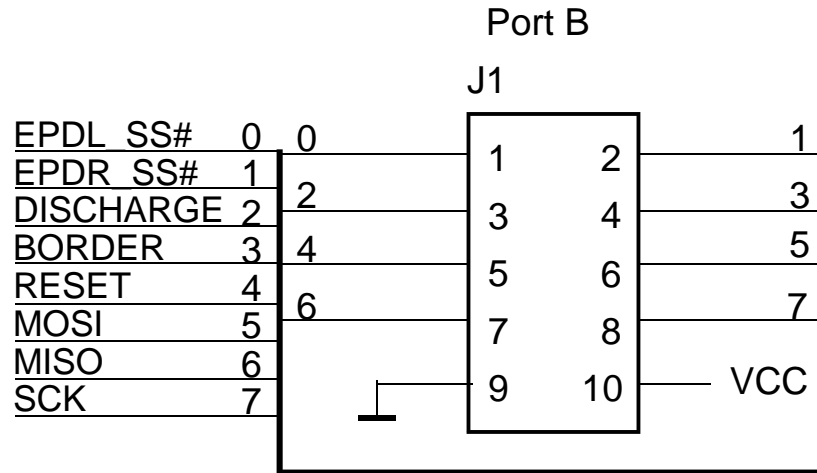
*: BORDER and EPD_PWM are pulses generated by counter/timer units

** : The I2C bus is not supported here.

Here it is shown, how an example microcontroller could be connected to the ports of the E-Paper trainer.

E-Paper Trainer Type 1

Dual Embedded Artists 2.7 inch
E-paper Display Modules
Example microcontroller attachment
Sheet 1 of 4
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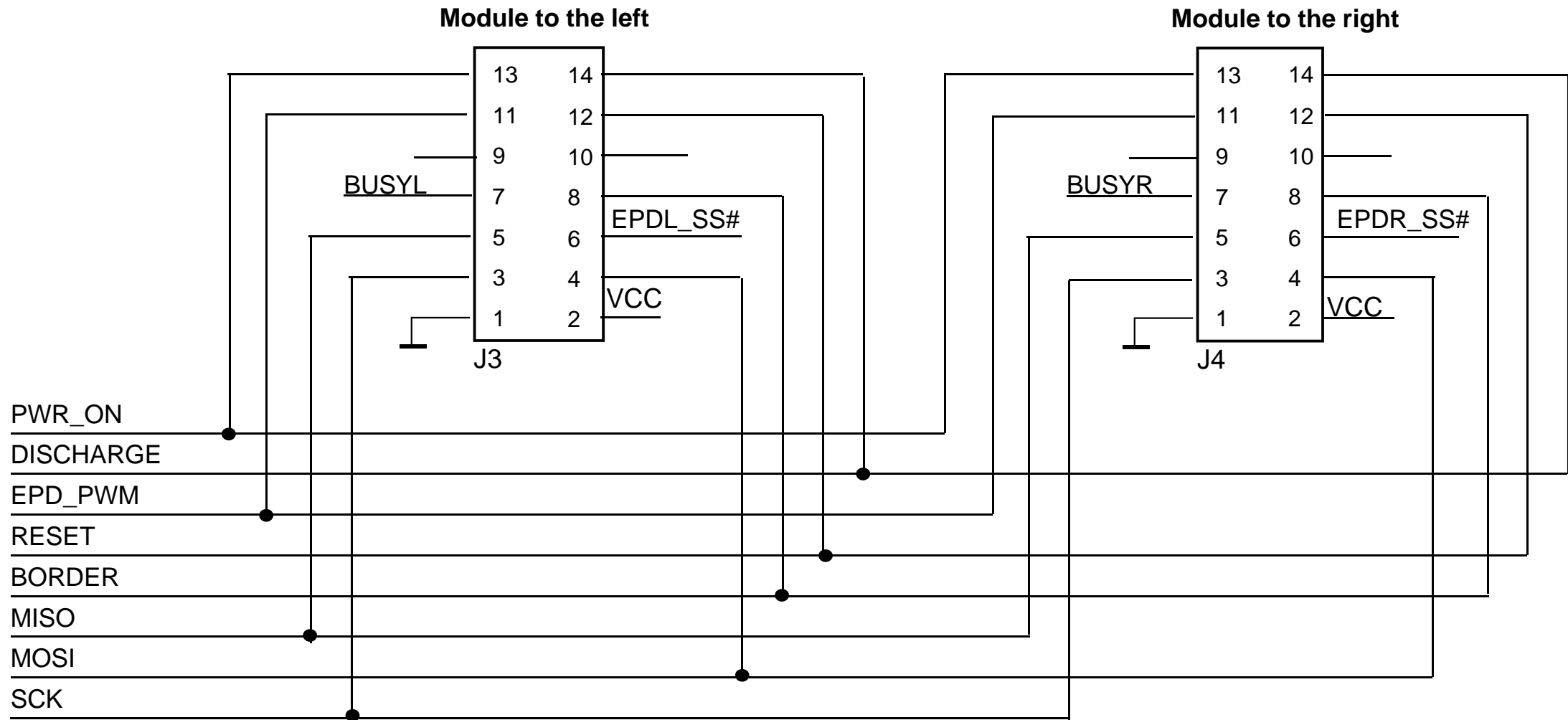
E-Paper Trainer Type 1

Dual Embedded Artists 2.7 inch
E-paper Display Modules

I/O port connectors

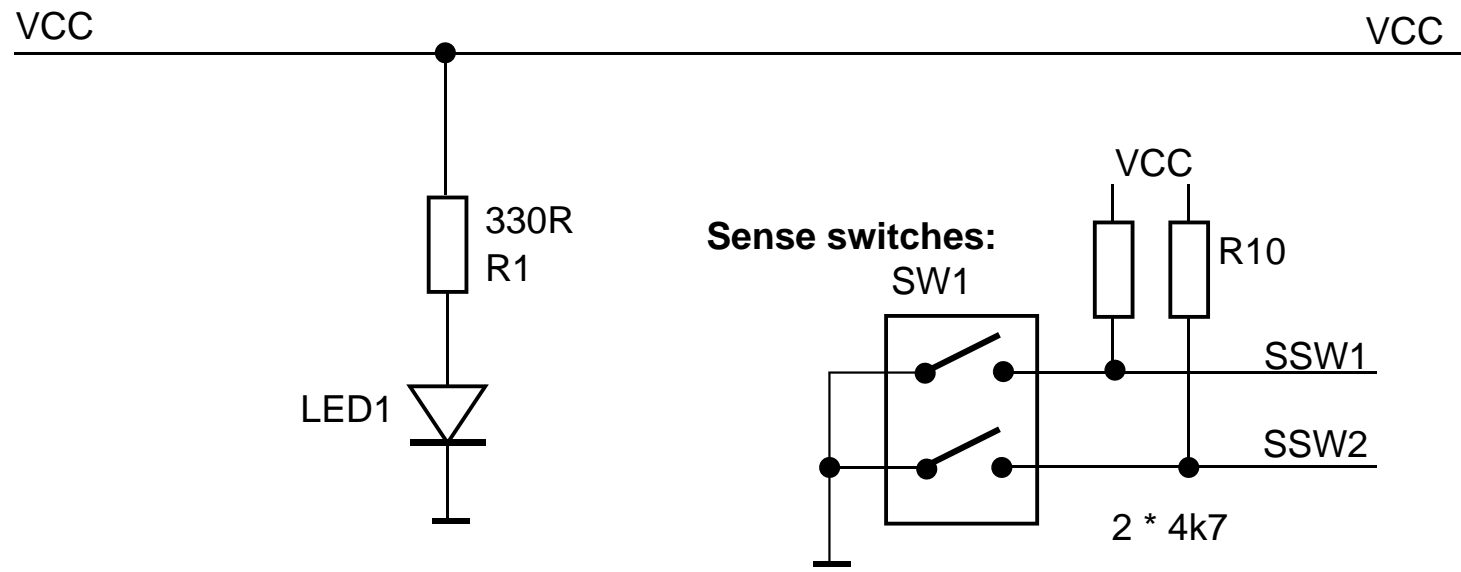
Sheet 2 of 4

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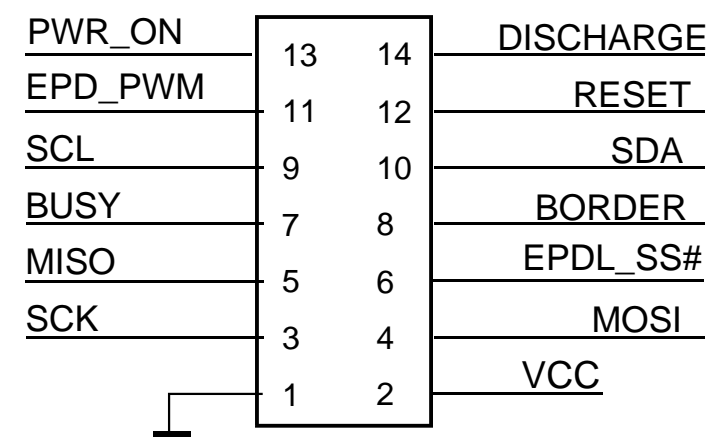


Panels = EM027AS012.
 The I2C bus is not supported here.
 EPDL_SS# = Select the module to the left.
 EPDR_SS# = Select the module to the right.

E-Paper Trainer Type 1
 Dual Embedded Artists 2.7 inch
 E-paper Display Modules
 Display module connectors
 Sheet 3 of 4
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The principal pinout of each EPLD connector:



E-Paper Trainer Type 1
 Dual Embedded Artists 2.7 inch
 E-paper Display Modules
 Power supply, sense switches
 Sheet 4 of 4
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